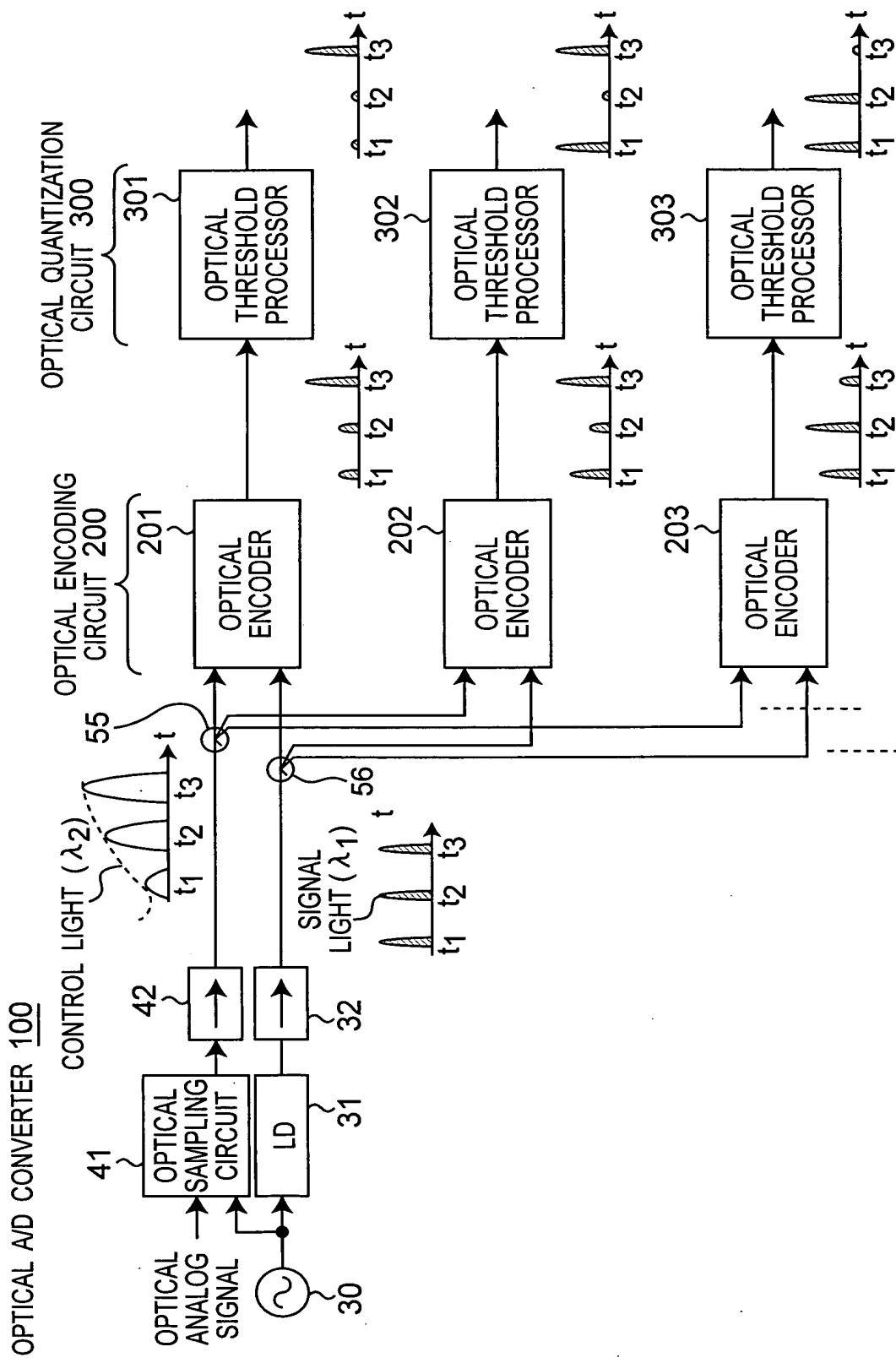


Fig. 2



*Fig. 3*

OPTICAL ENCODER 201

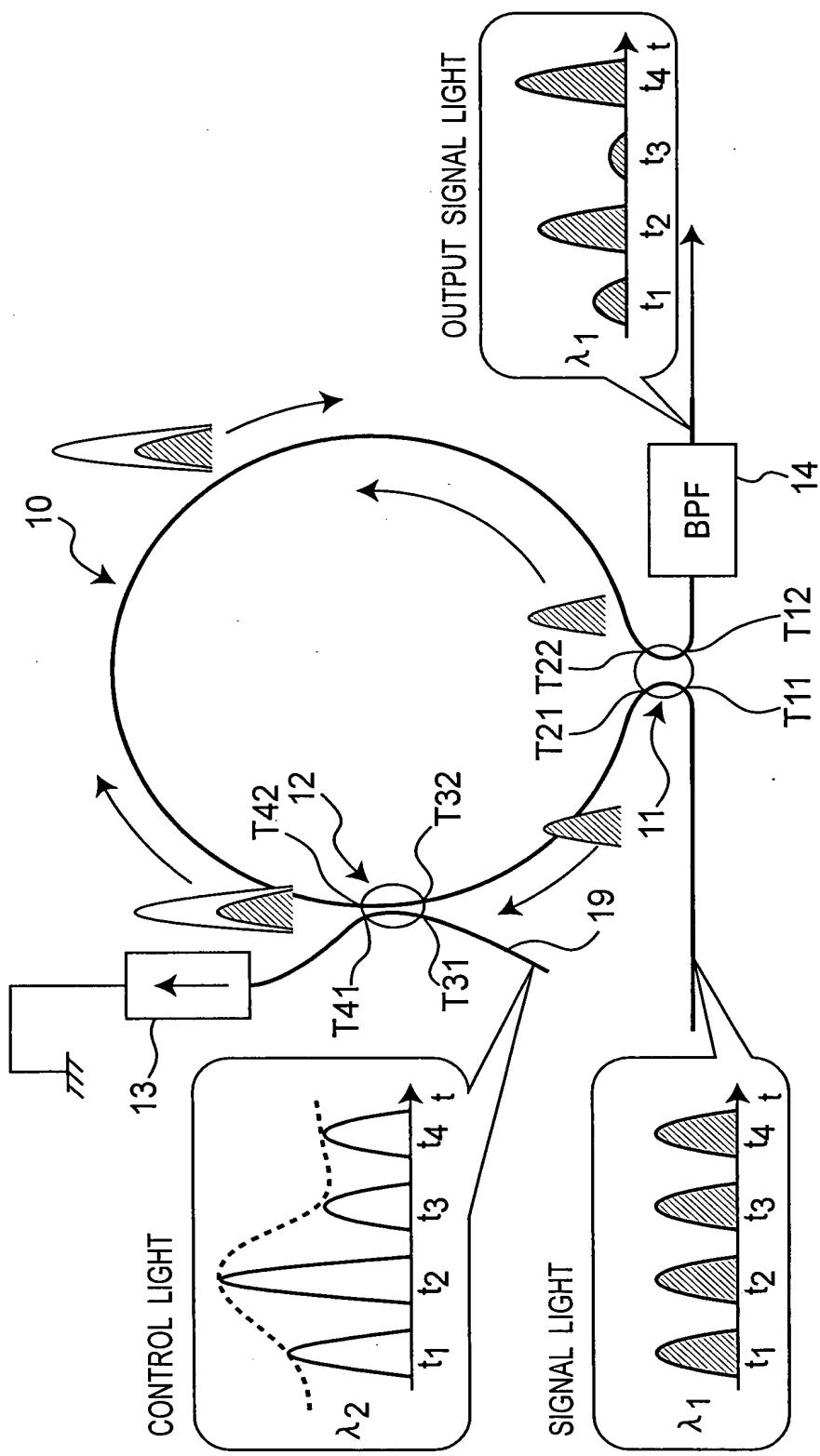


Fig.4

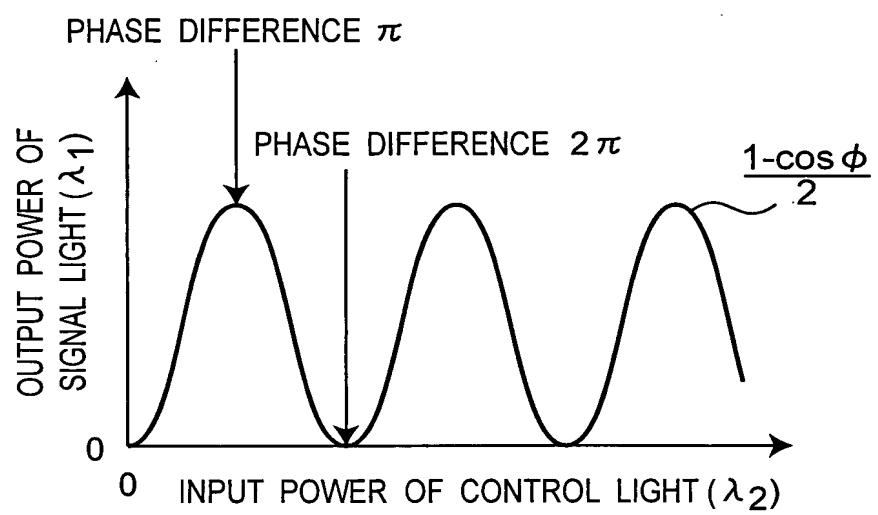


Fig. 5

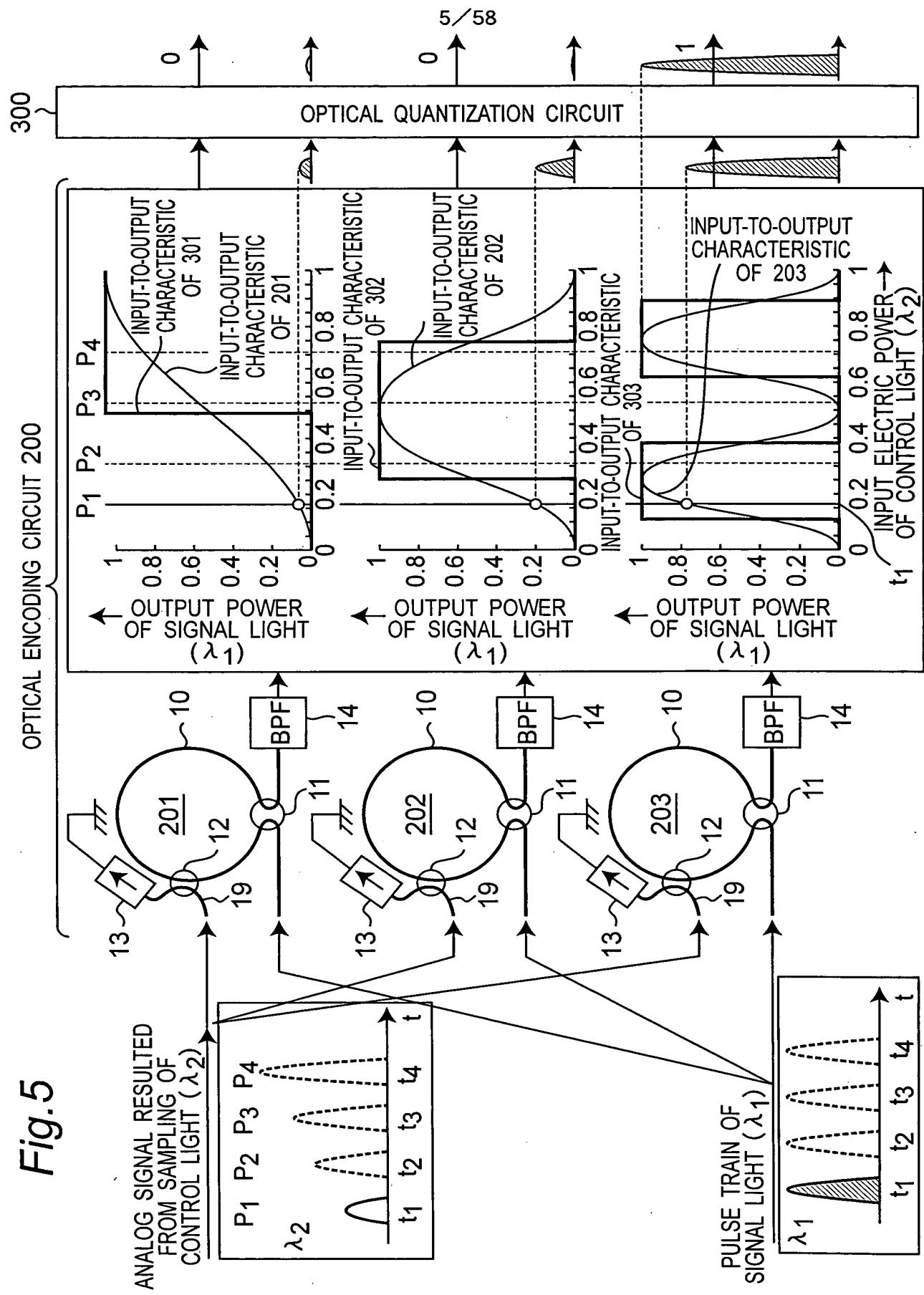


Fig. 6

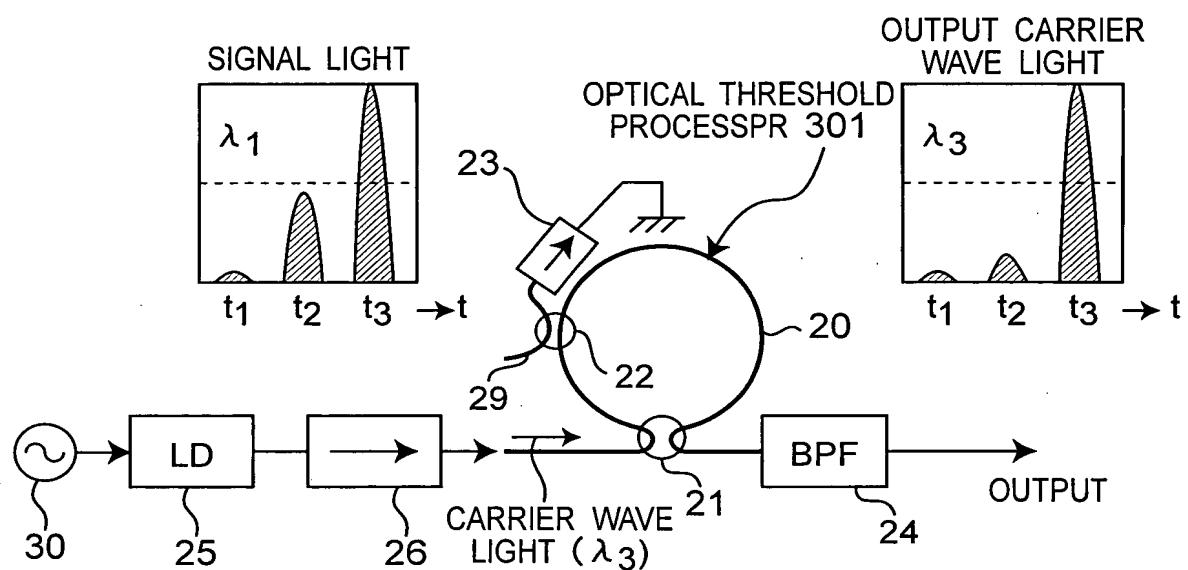


Fig. 7

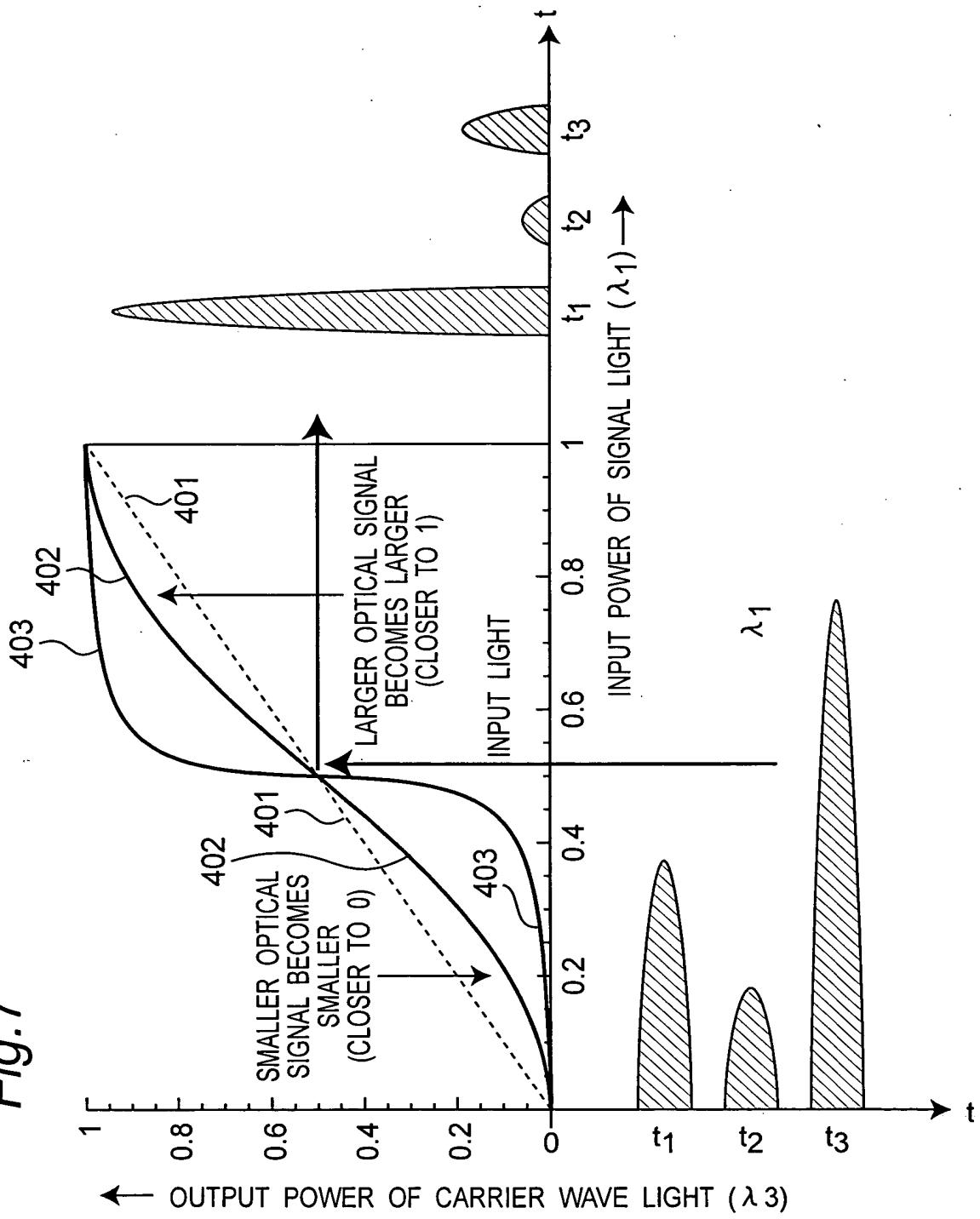


Fig. 8

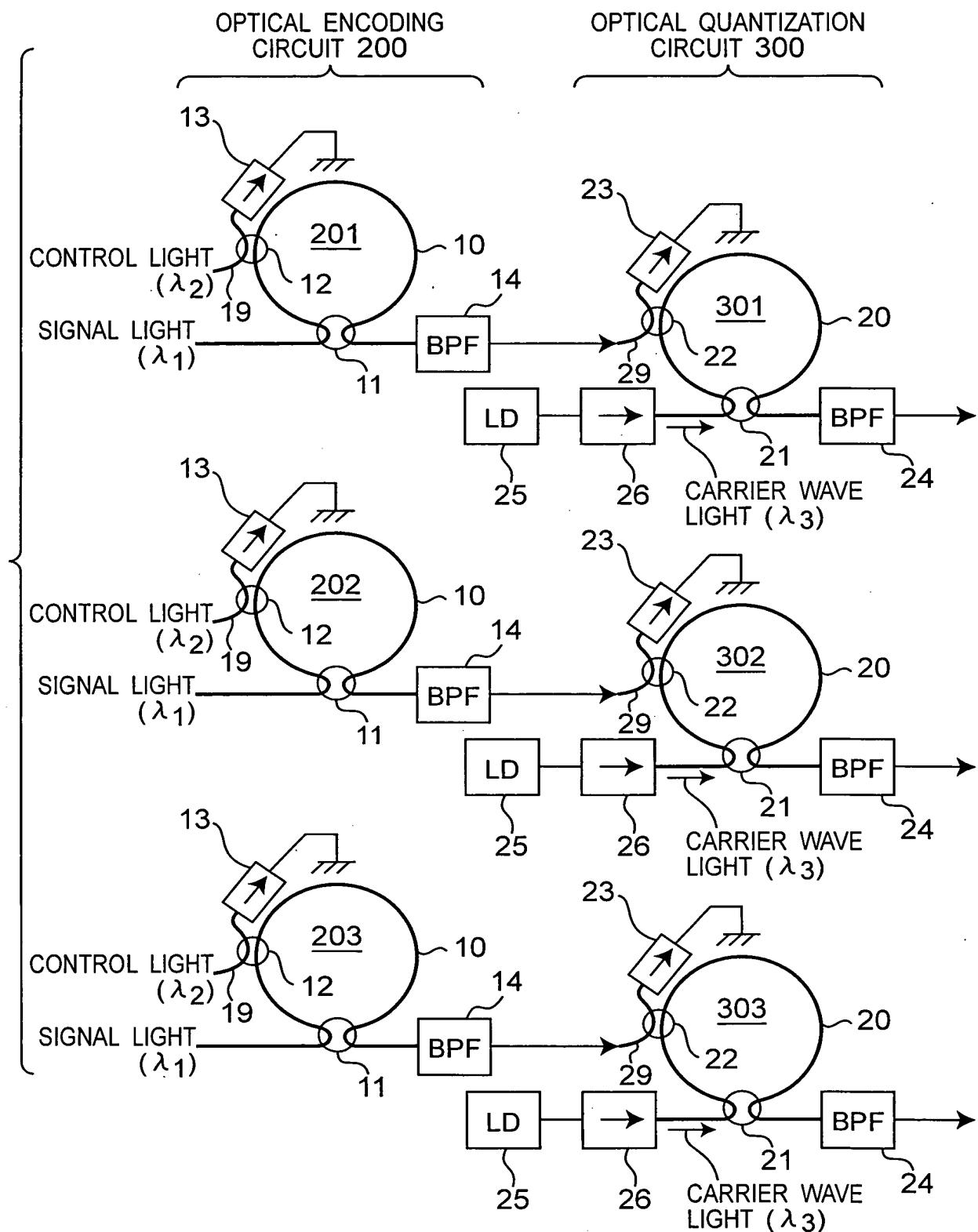


Fig. 9

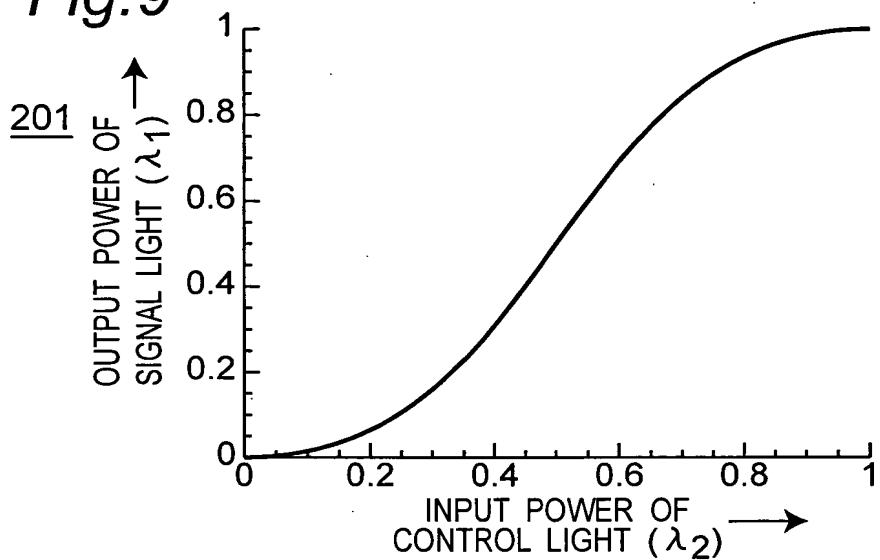


Fig. 10

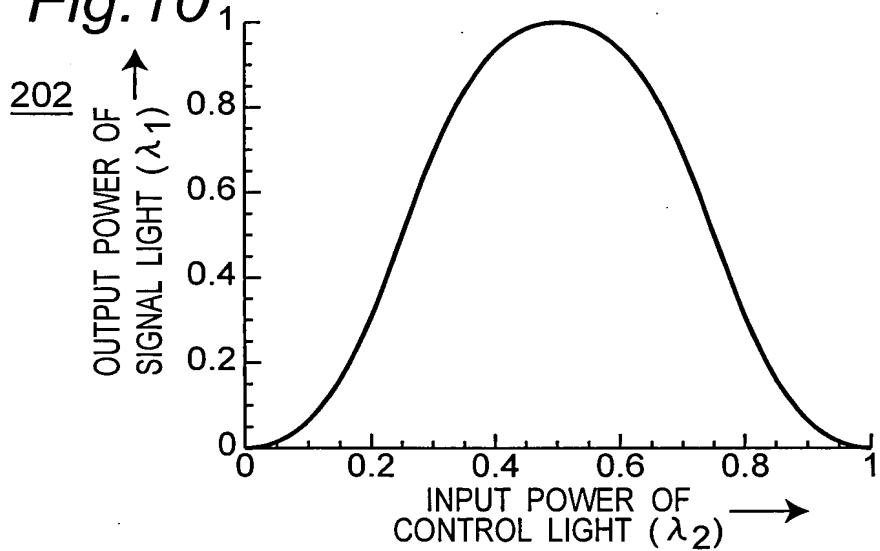


Fig. 11

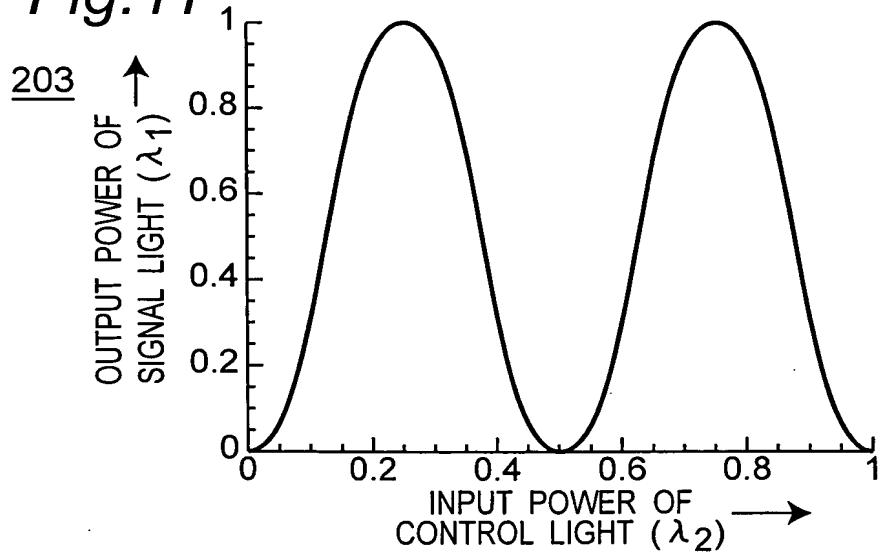


Fig. 12

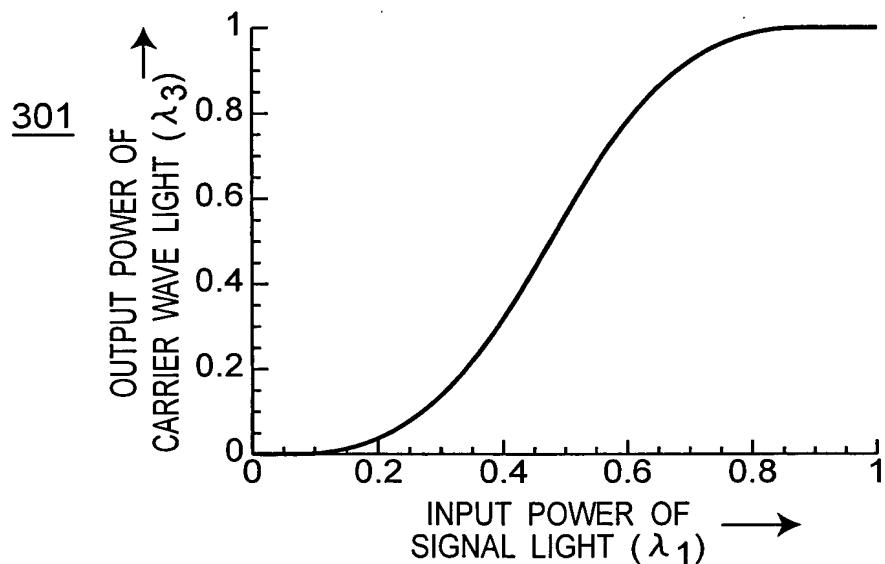


Fig. 13

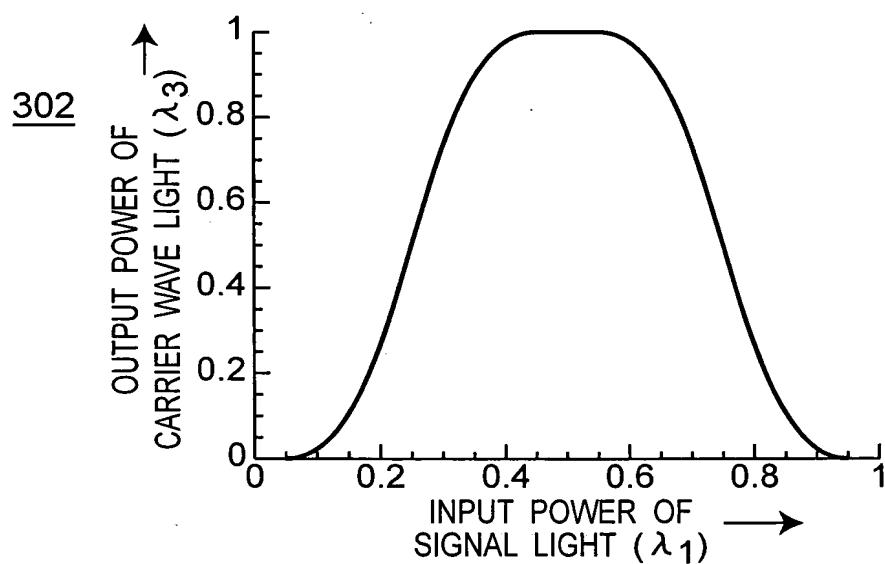


Fig. 14

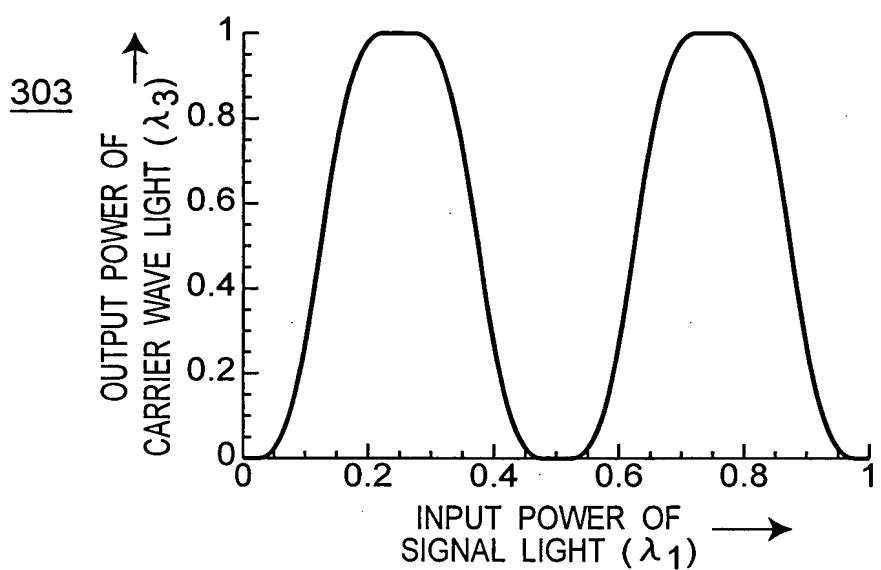


Fig. 15

MODIFIED PREFERRED EMBODIMENT  
OPTICAL A/D CONVERTER 100A  
OPTICAL ENCODING CIRCUIT 200  
OPTICAL QUANTIZATION CIRCUIT 300A

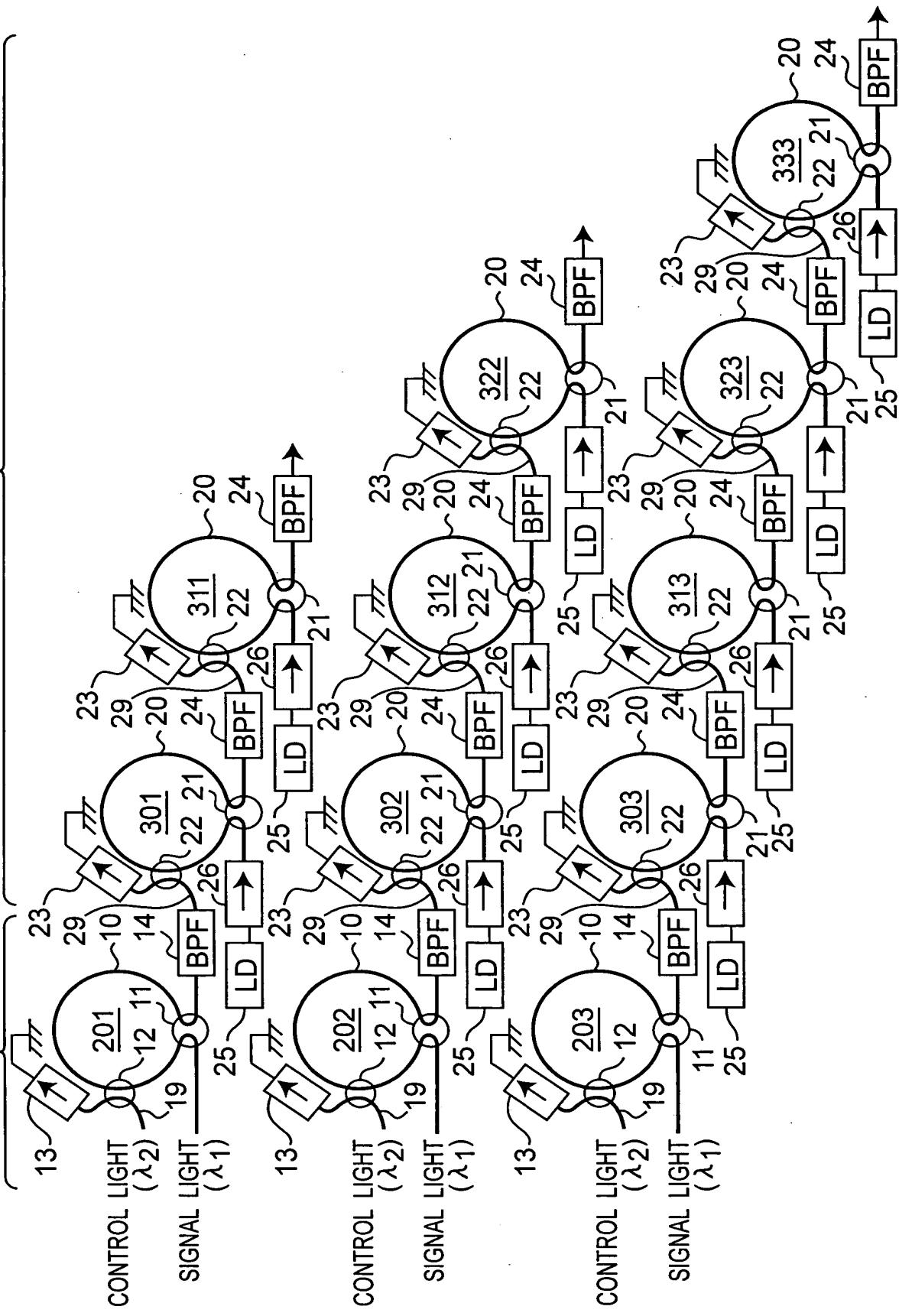


Fig. 16

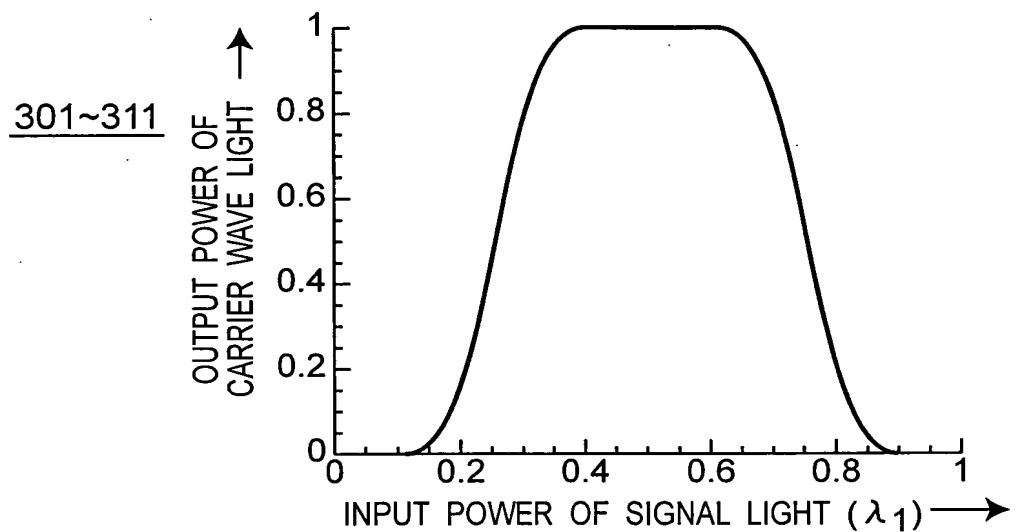
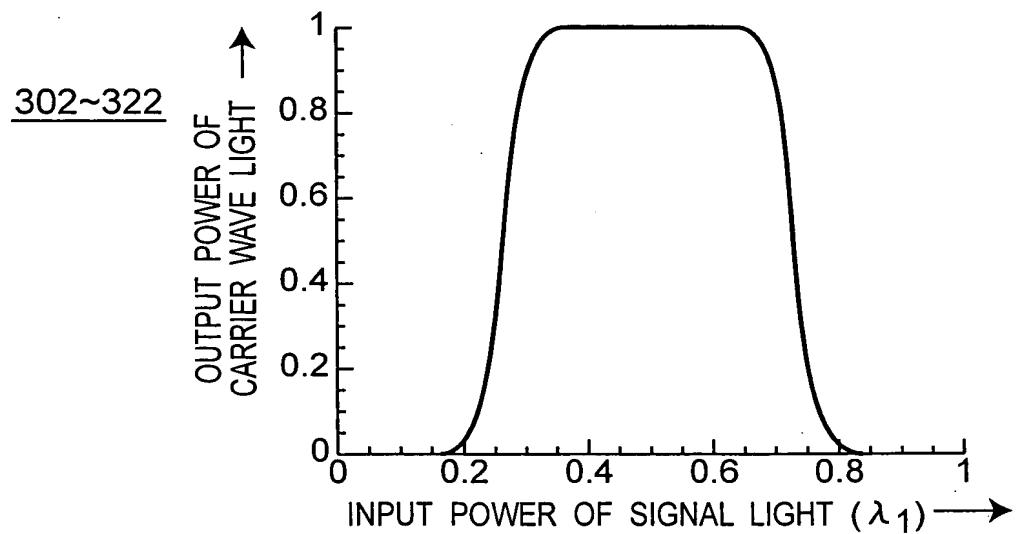
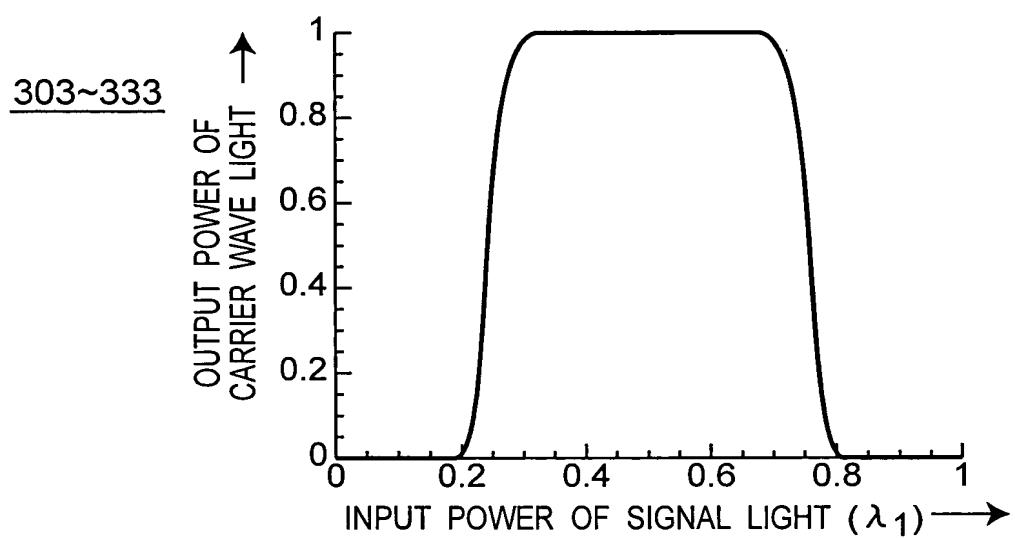


Fig. 17



*Fig. 18*



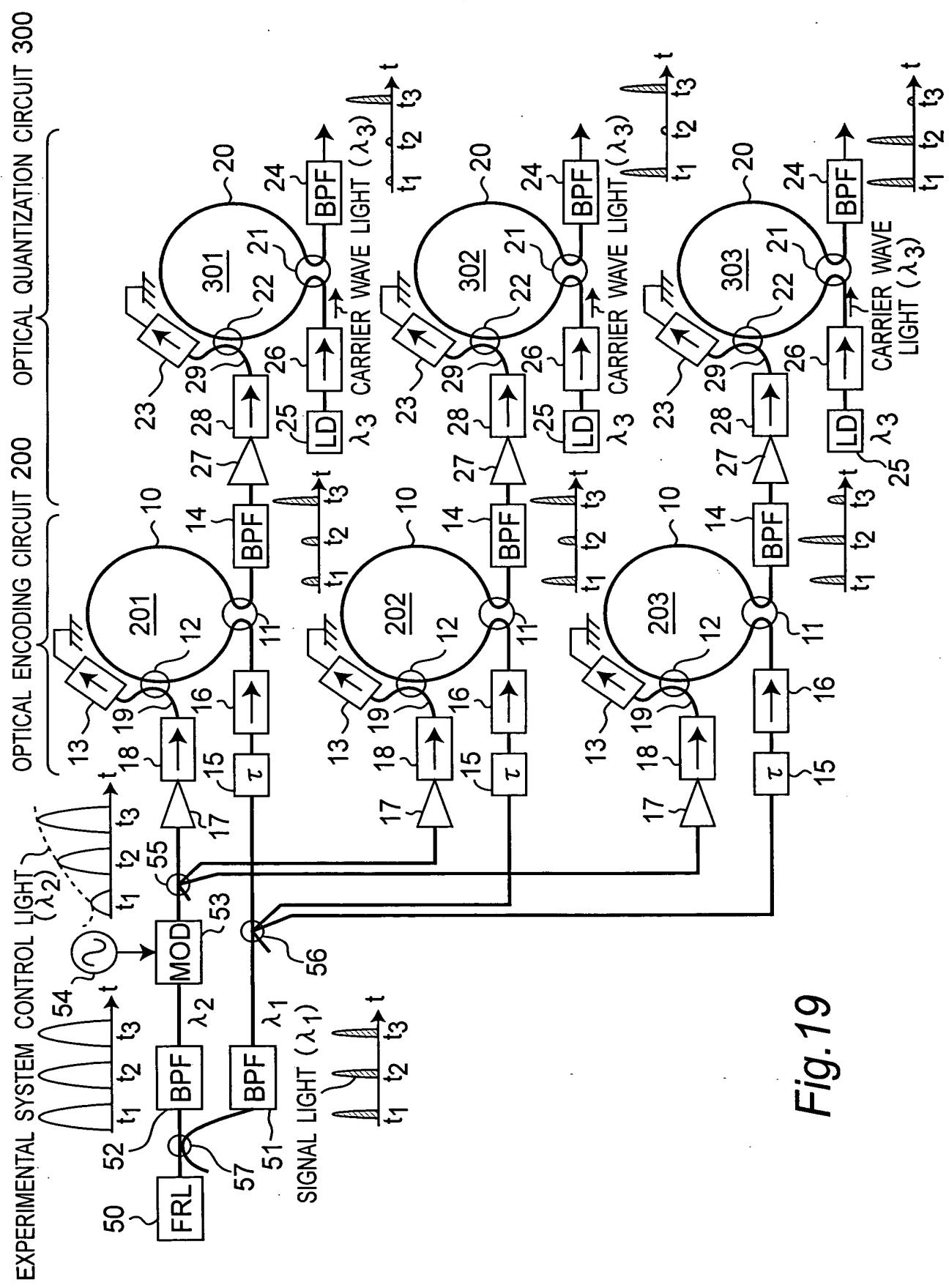


Fig. 19

Fig. 20

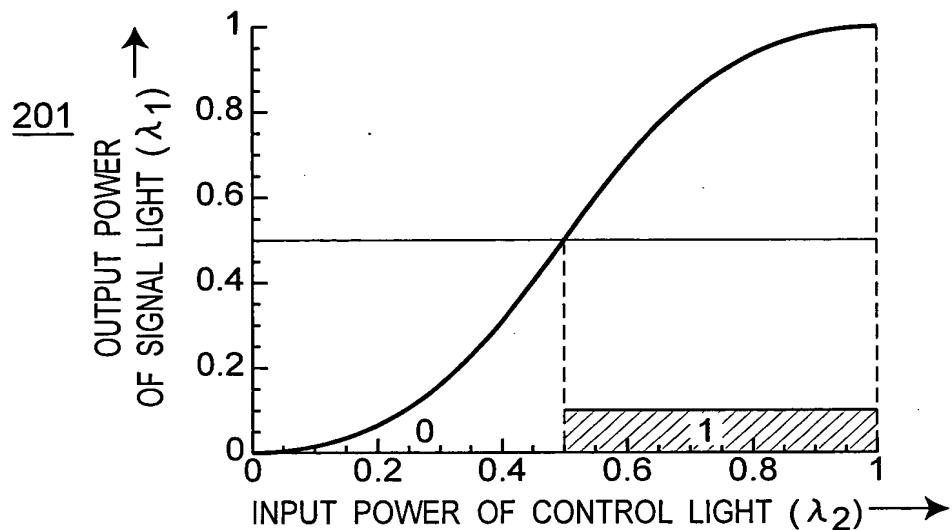


Fig. 21

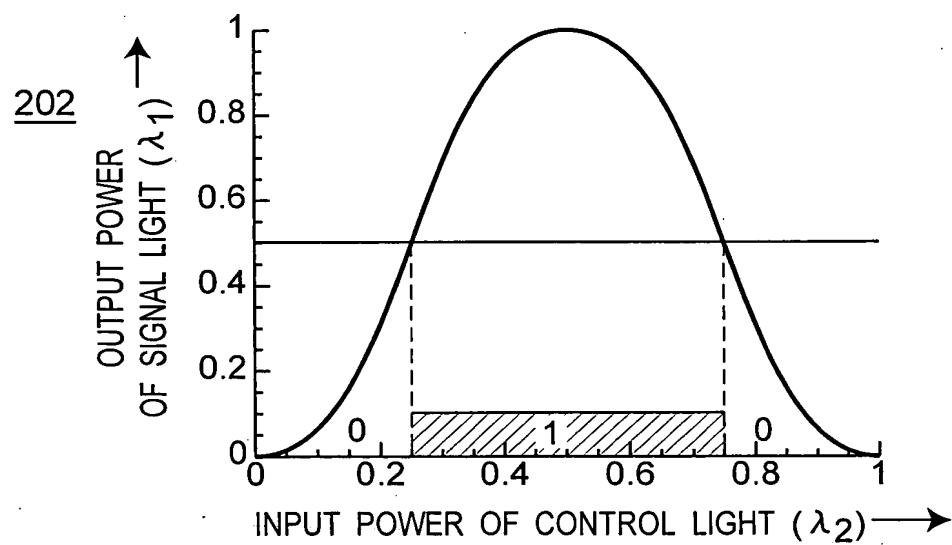


Fig.22

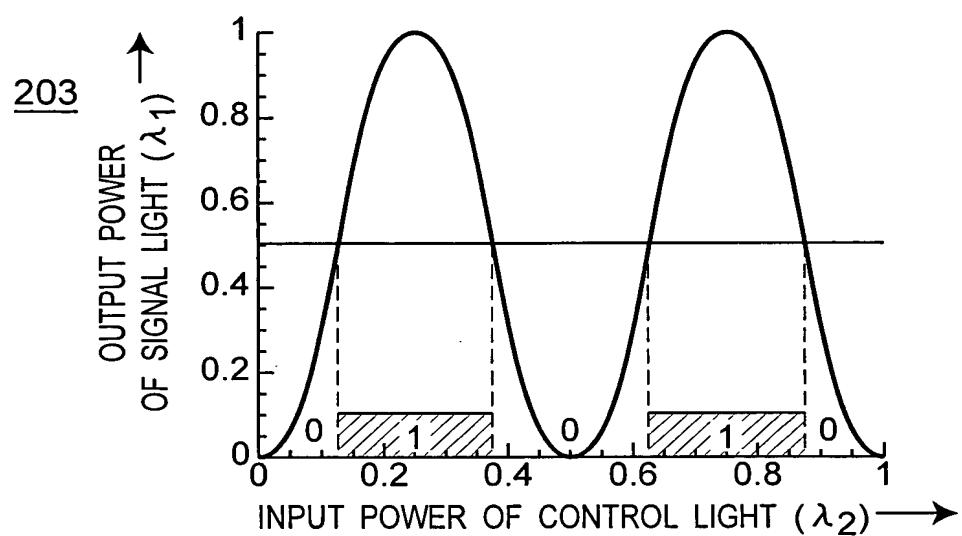


Fig.23

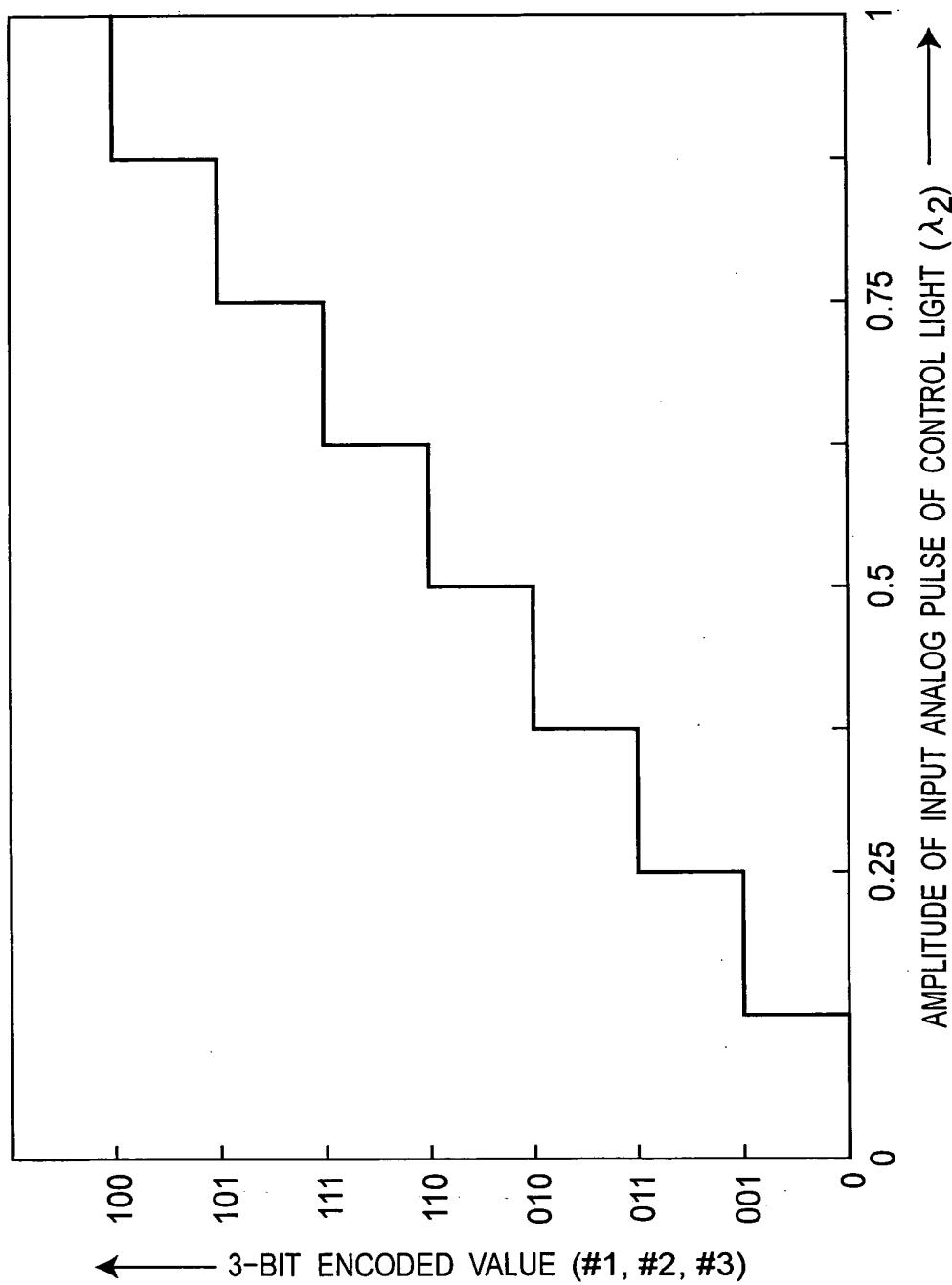


Fig. 24

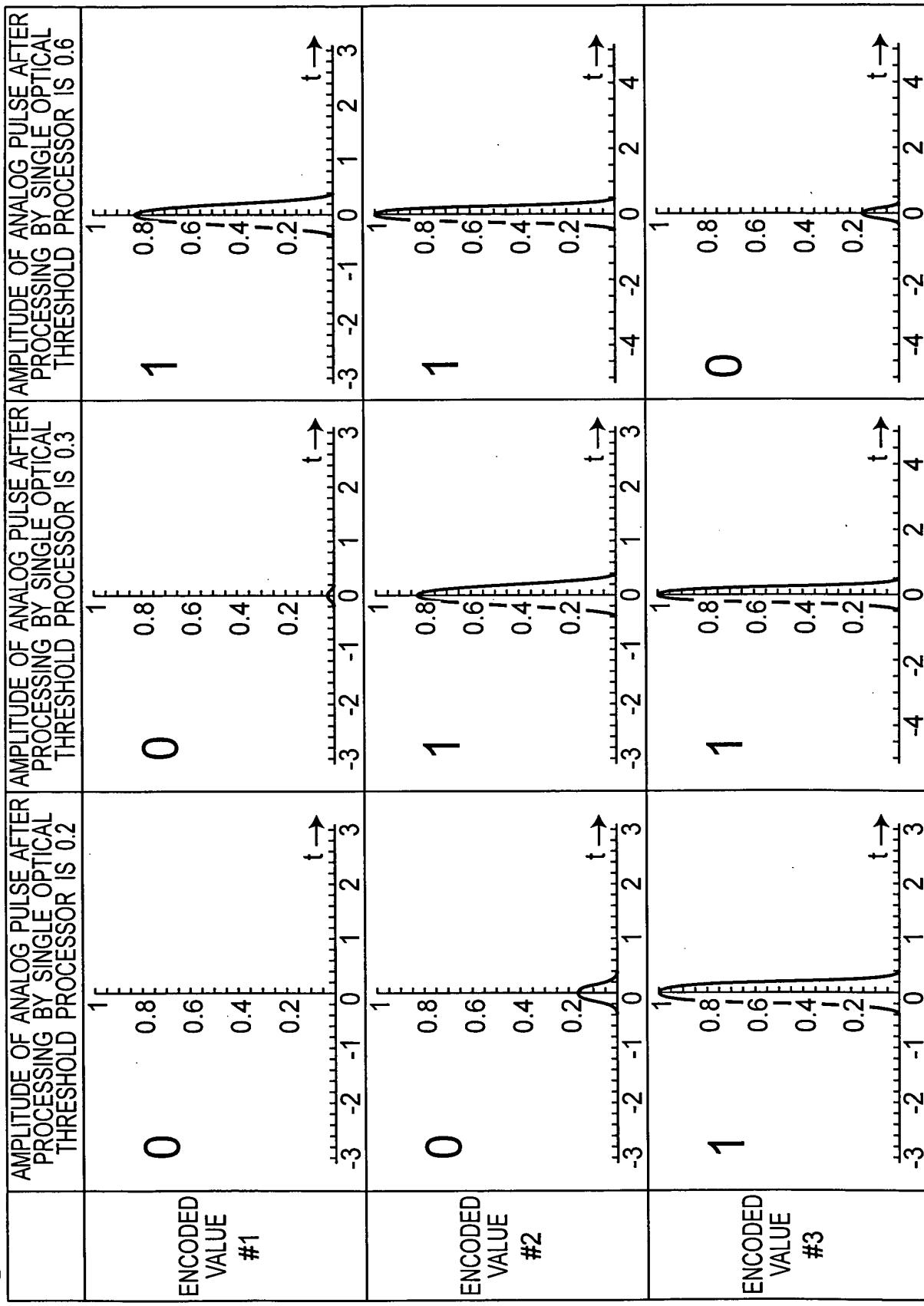


Fig.25

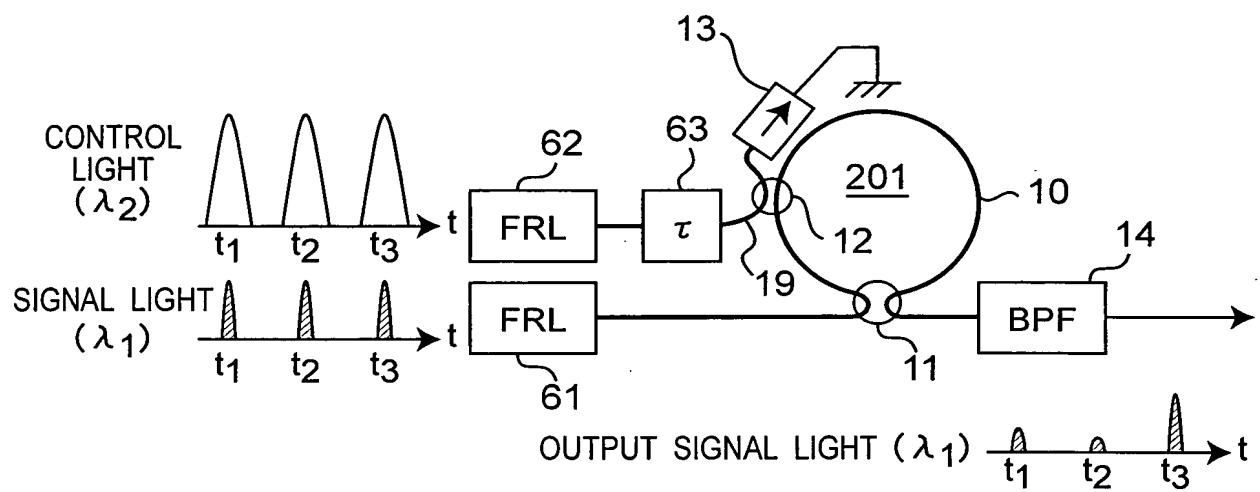


Fig. 26

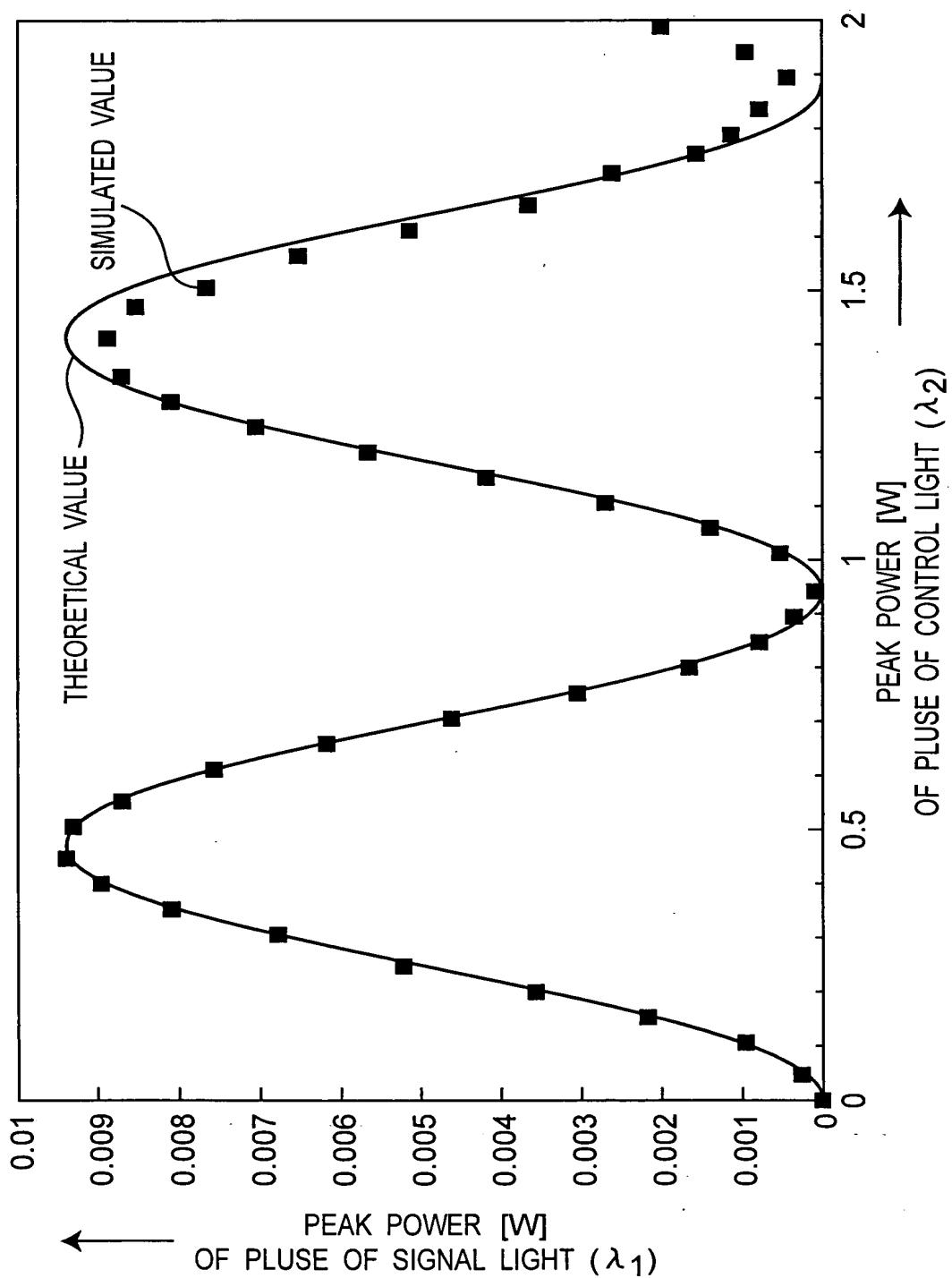


Fig. 27

Fig. Z/1

62

1 : 1/2 : 1/4

FRL

$\lambda_2$

$\lambda_1$

71

72

73

PEAK POWER

1W

The diagram shows a bridge circuit. The top horizontal line is labeled "1 : 1/2 : 1/4" with a brace. The left vertical line is labeled "62" with a brace. The right vertical line is labeled "PEAK POWER" and "1W". The bottom horizontal line is labeled "FRL". The top-left vertical line is labeled " $\lambda_2$ ". The top-right vertical line is labeled " $\lambda_1$ ". The middle-left vertical line is labeled "71". The middle-right vertical line is labeled "72". The bottom-right vertical line is labeled "73".

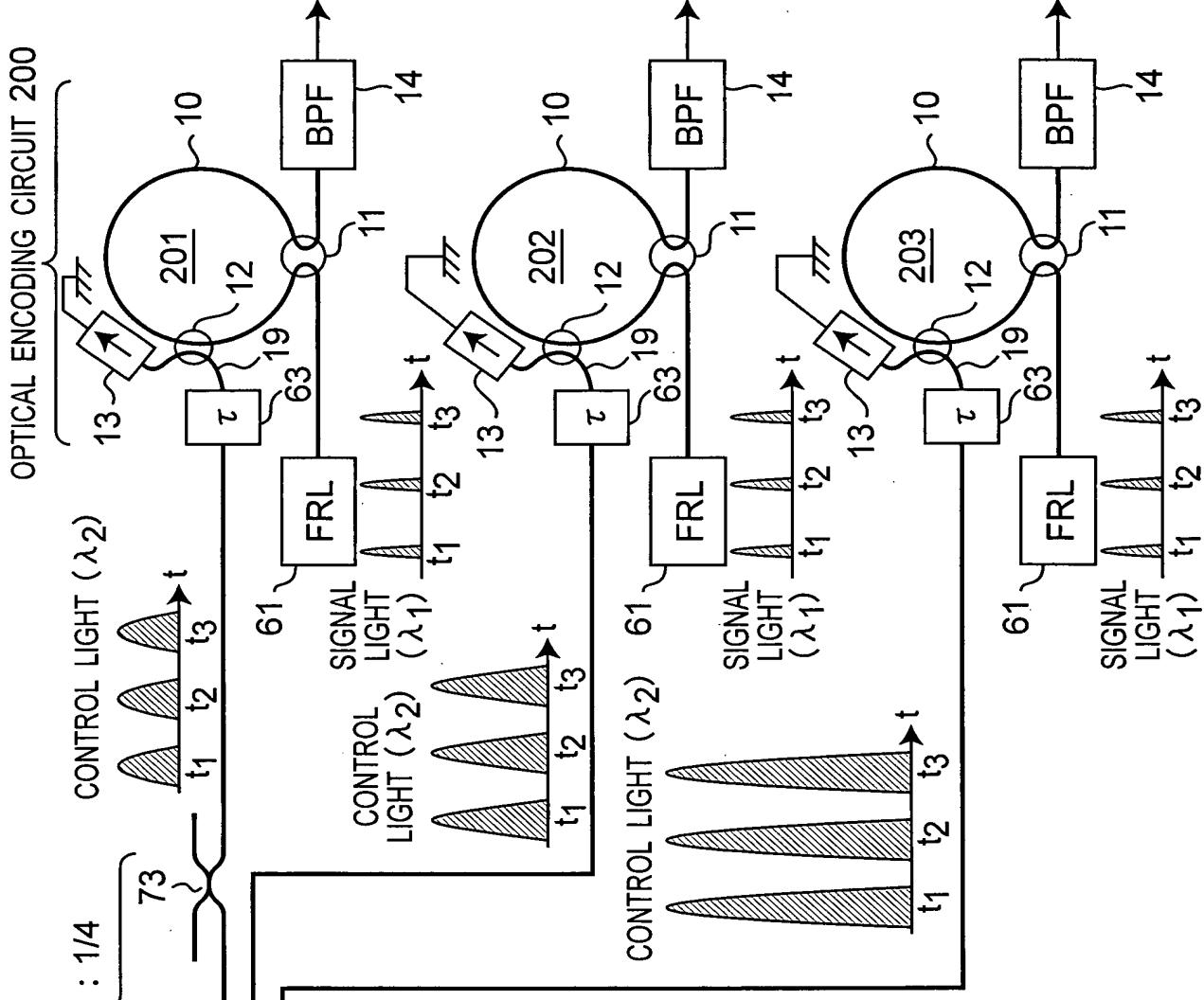


Fig.28

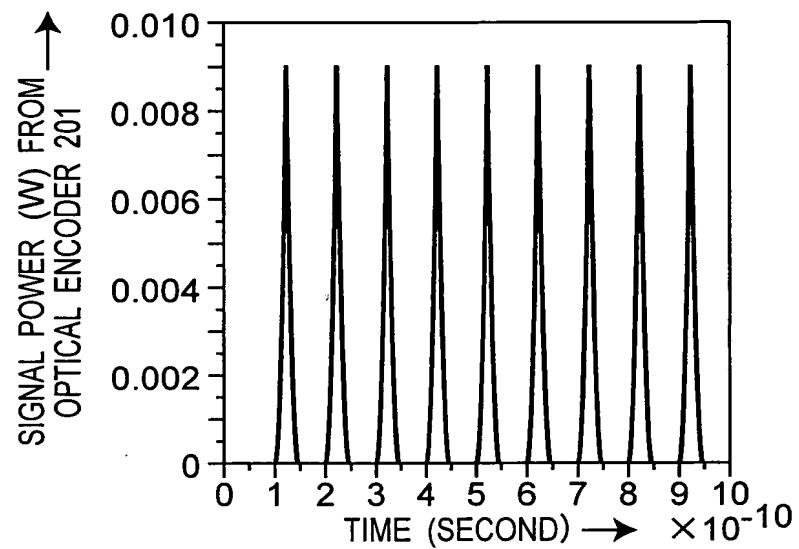
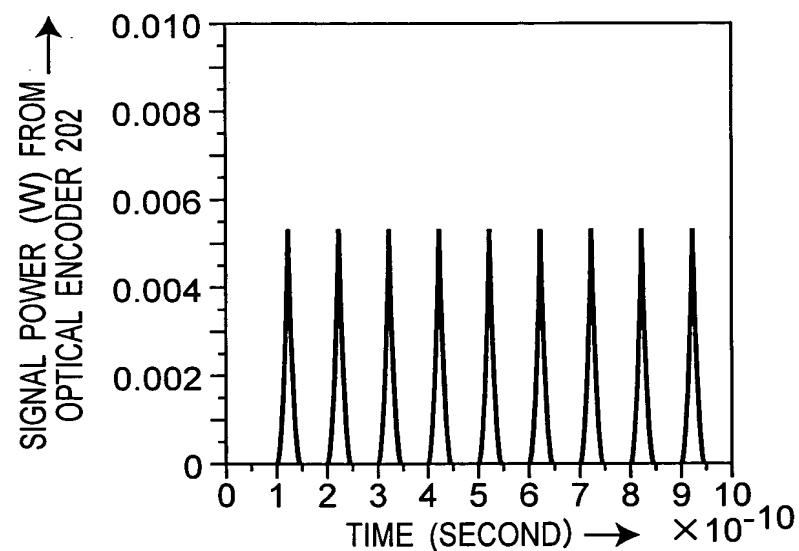
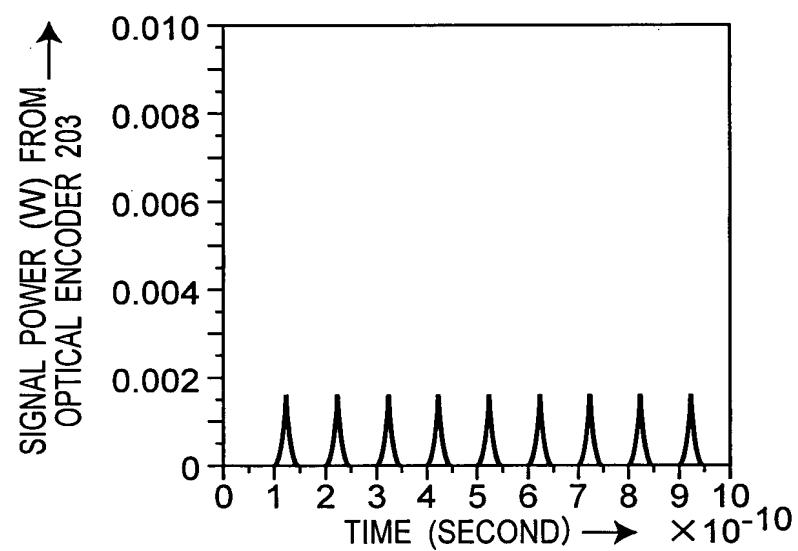
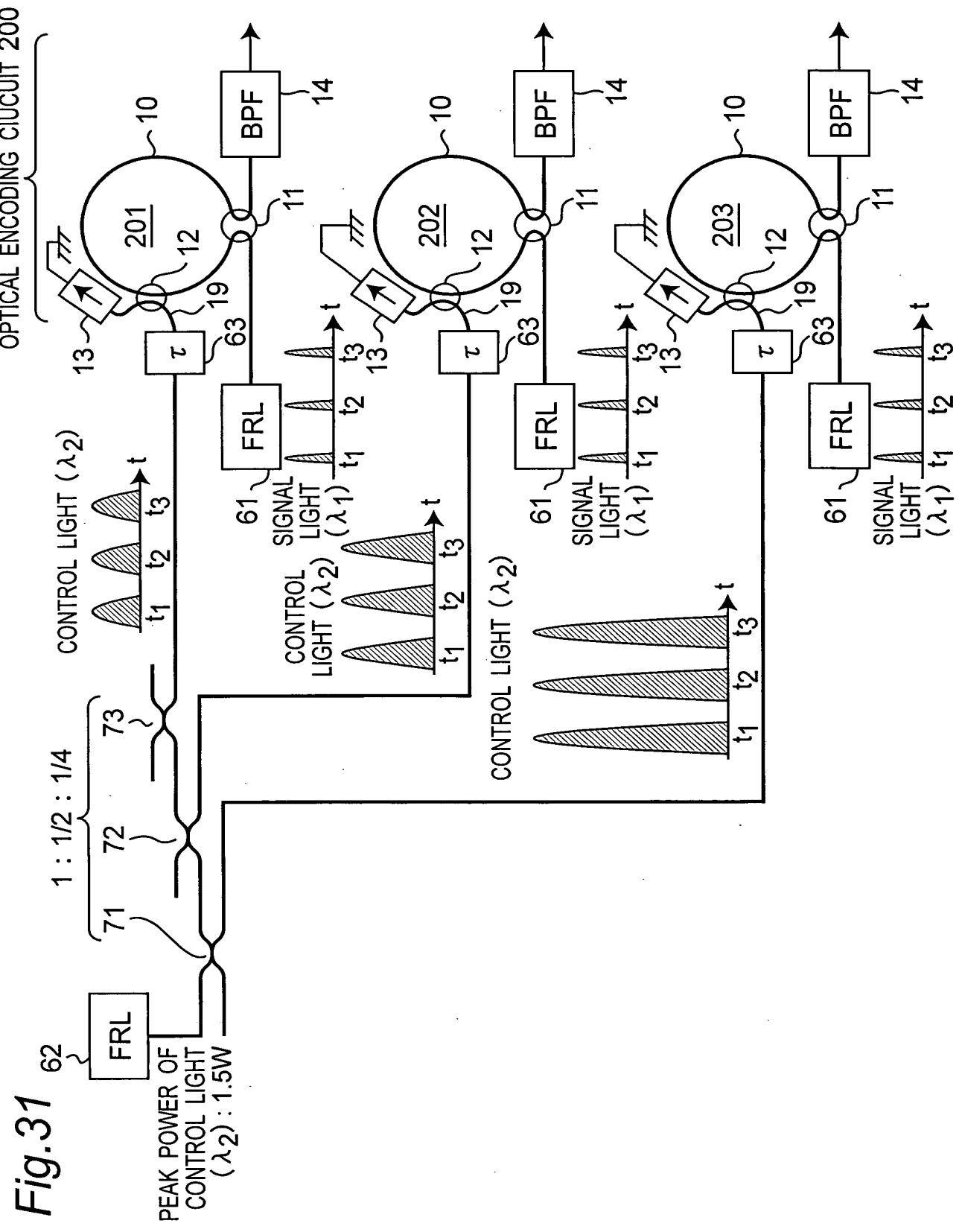


Fig.29

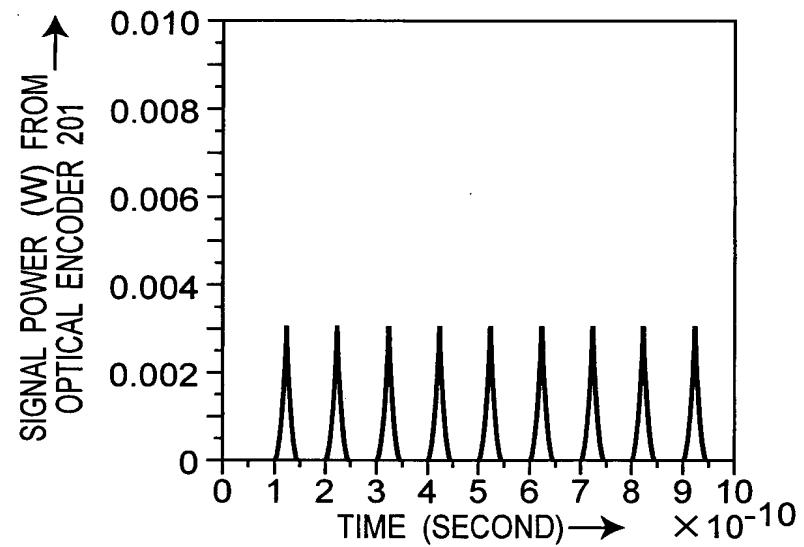


*Fig.30*





*Fig.32*



*Fig.33*

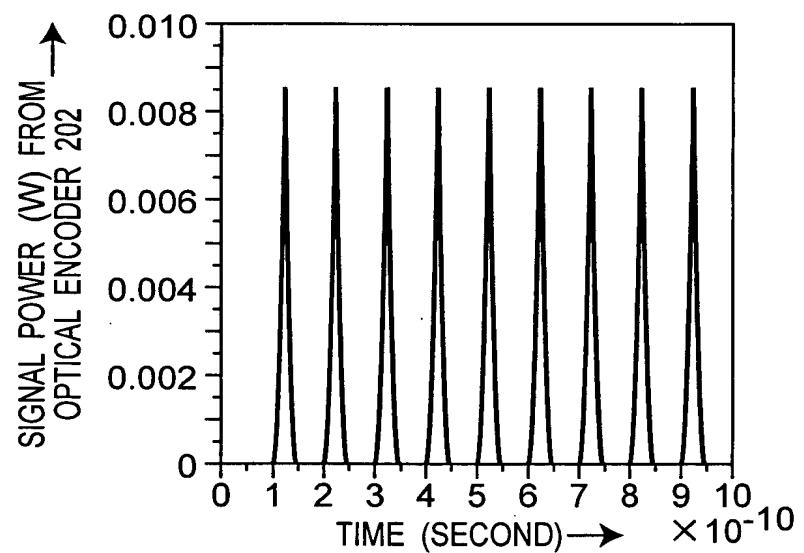
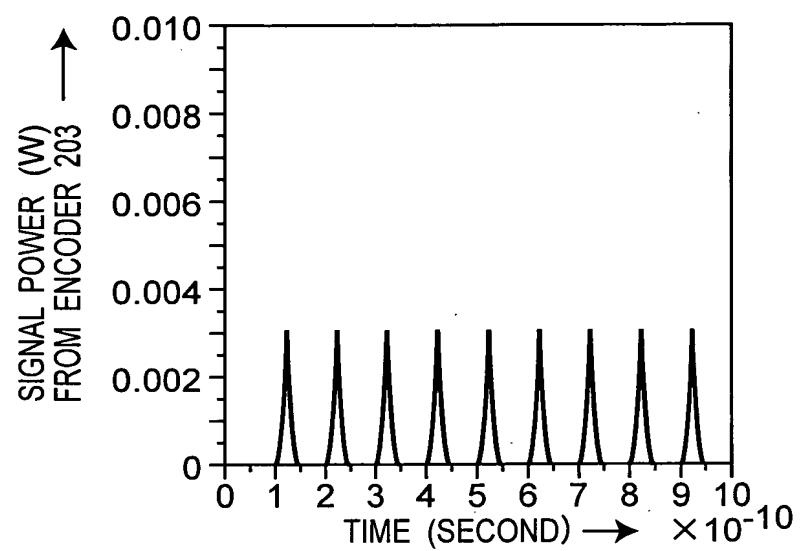


Fig.34



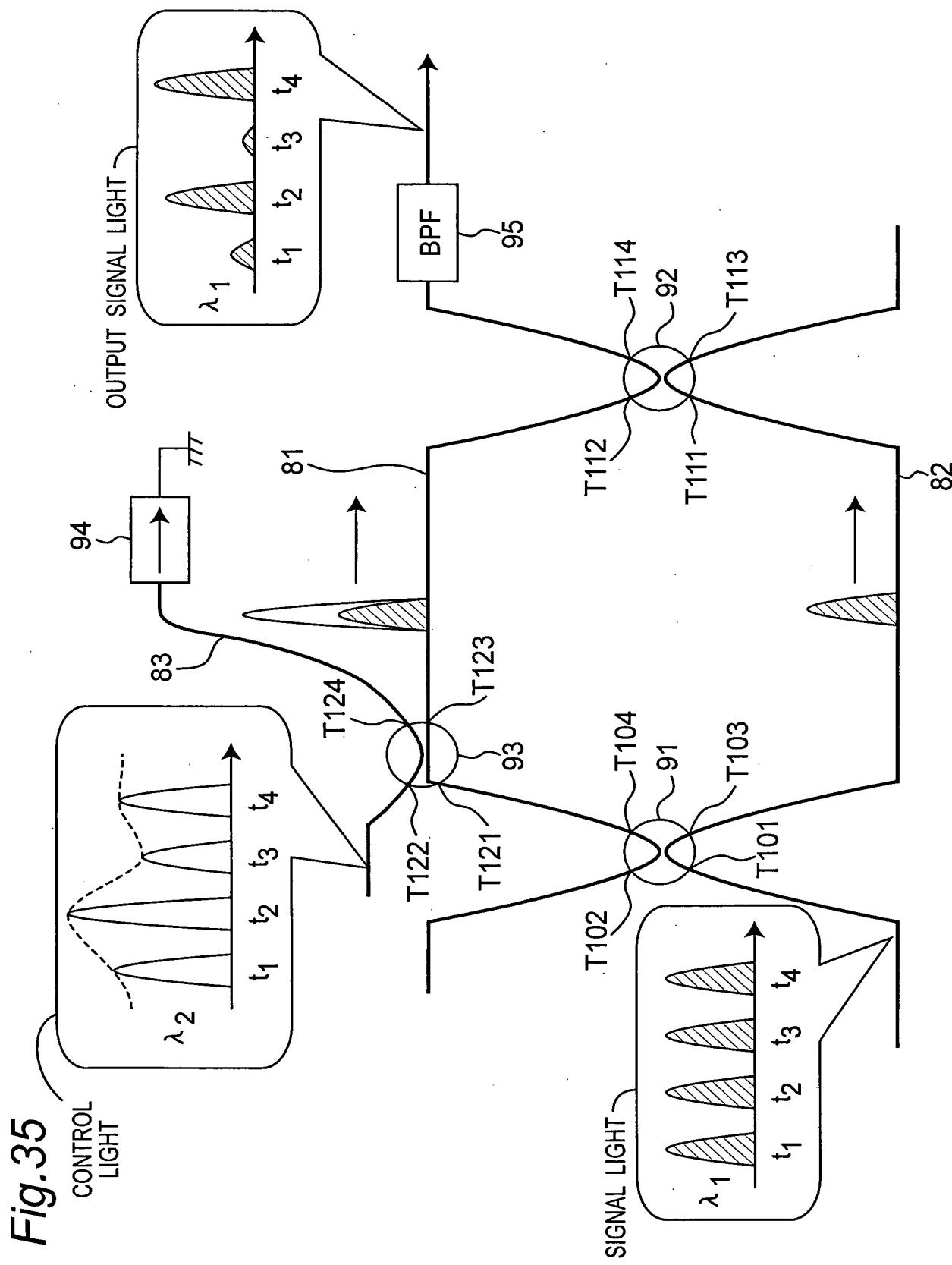
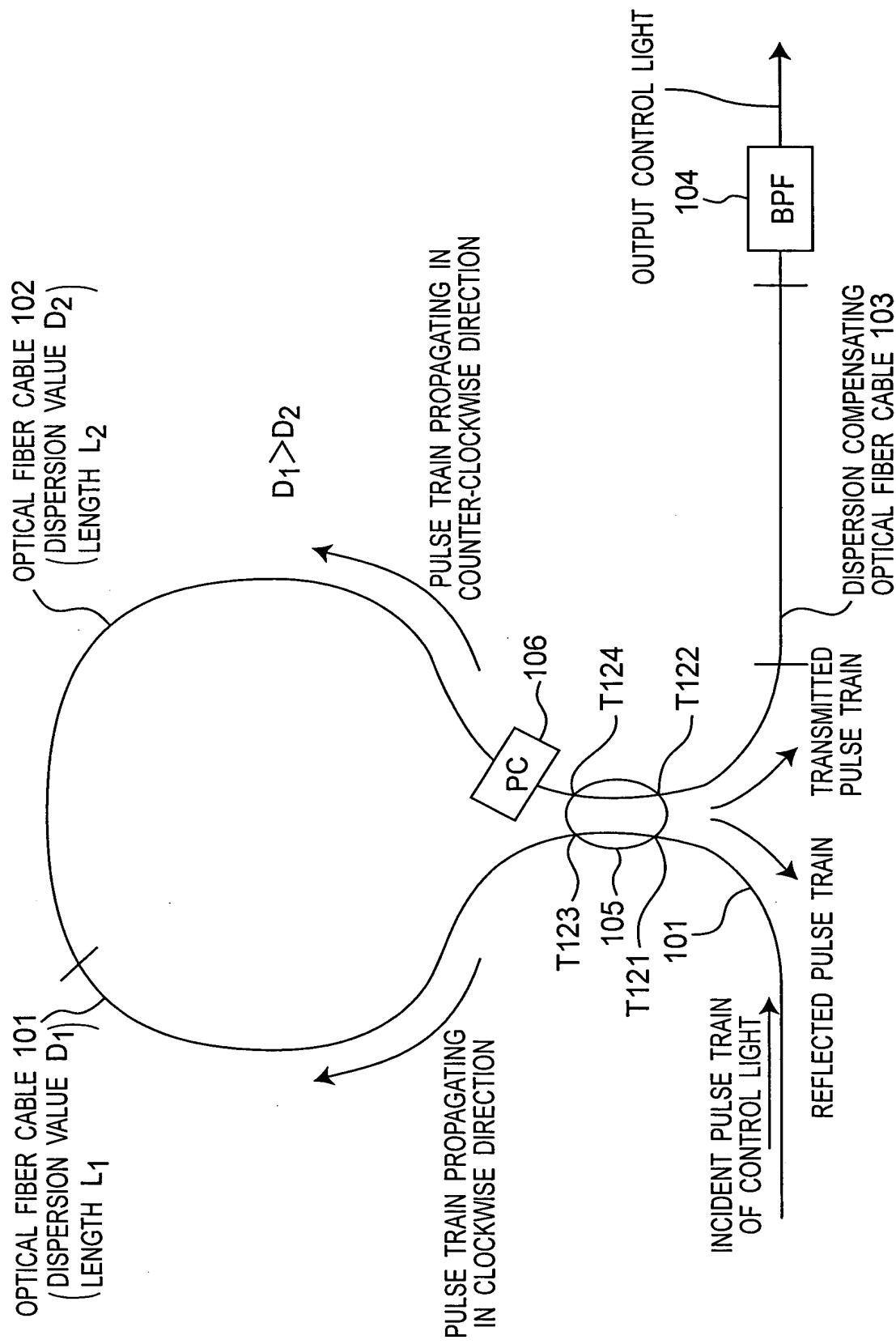


Fig.36



*Fig.37*

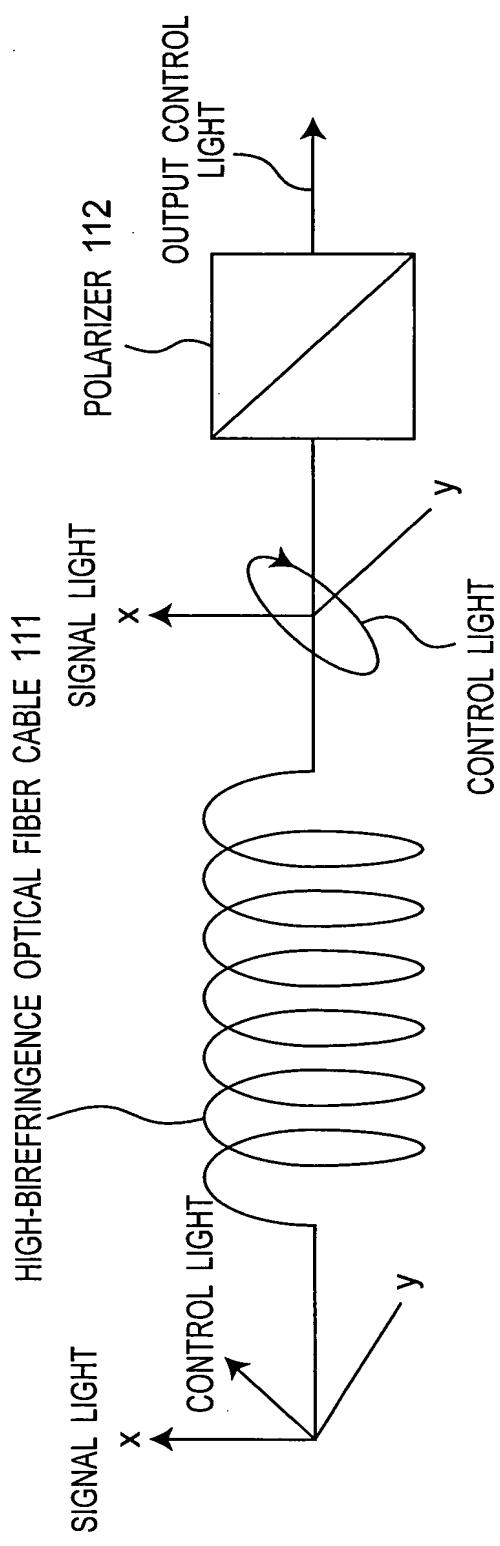


Fig. 38

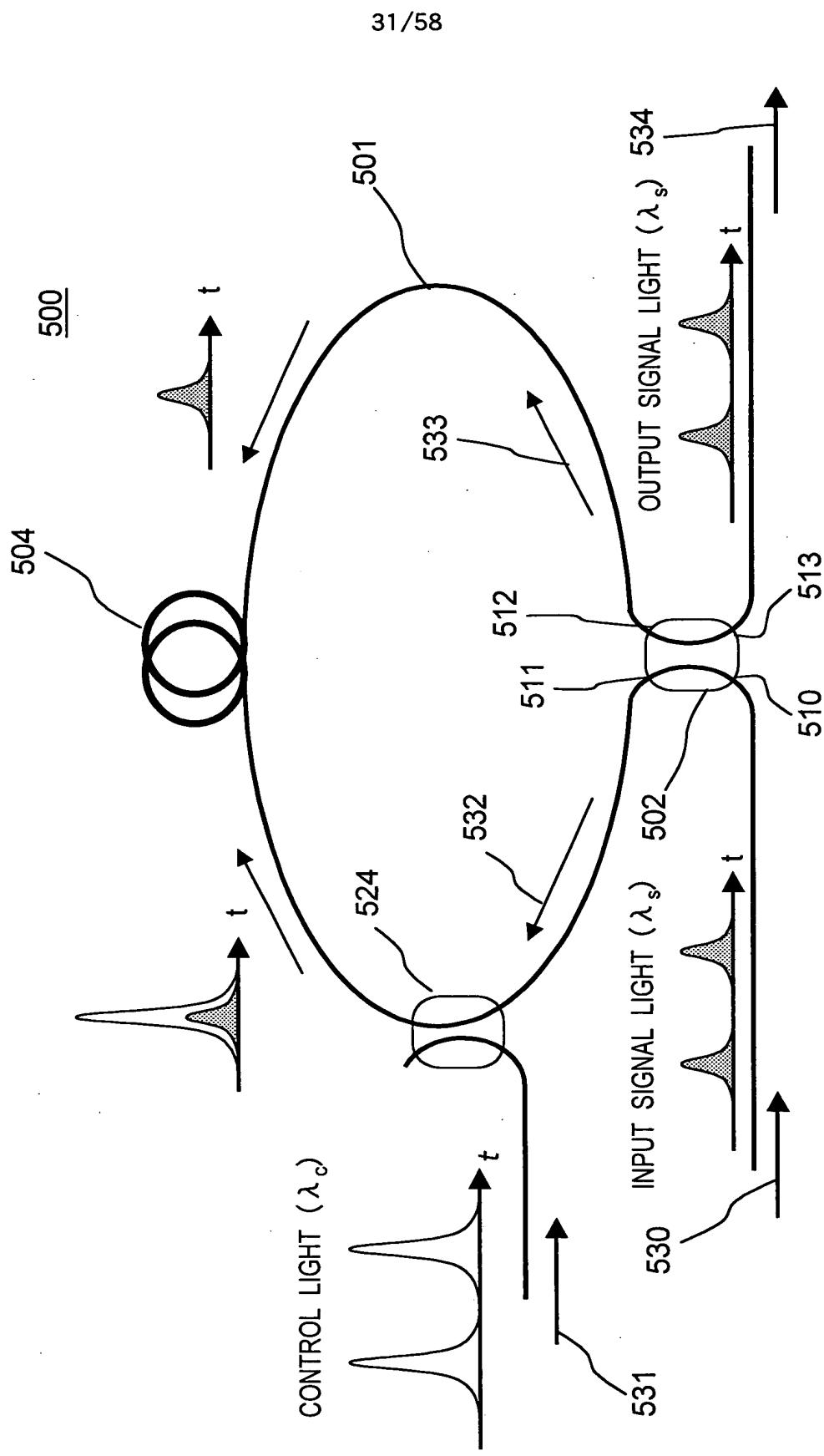
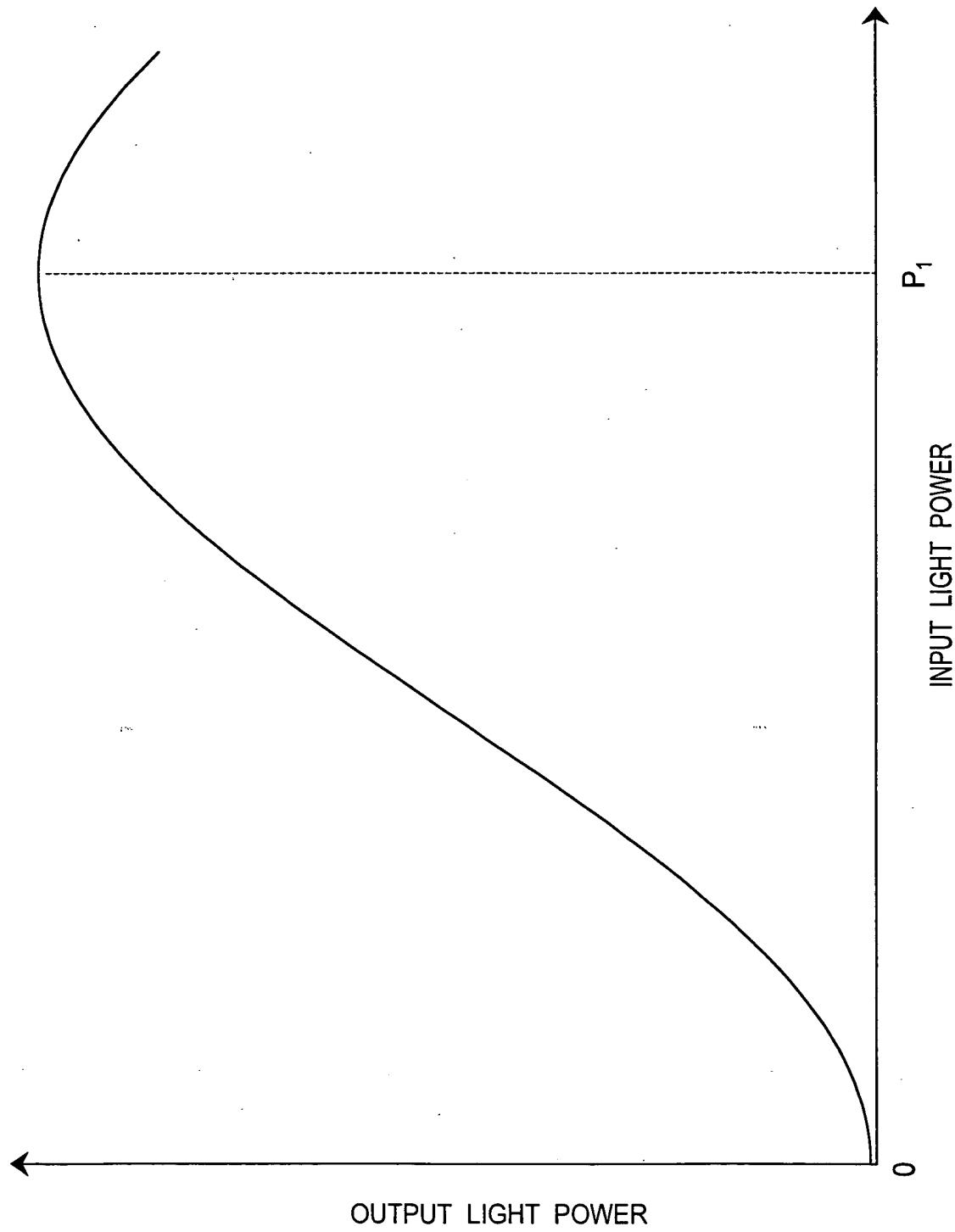


Fig. 39



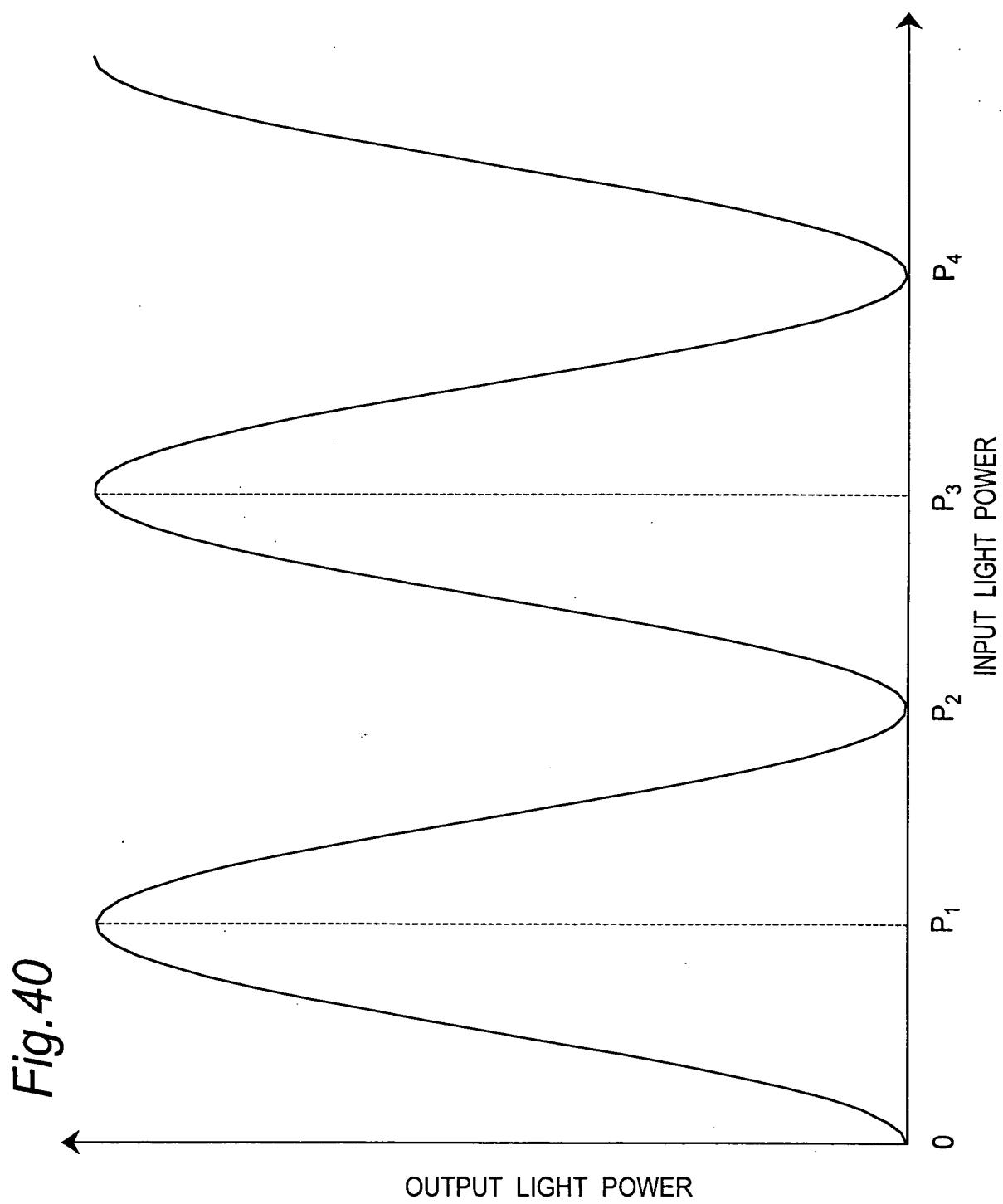


Fig. 41

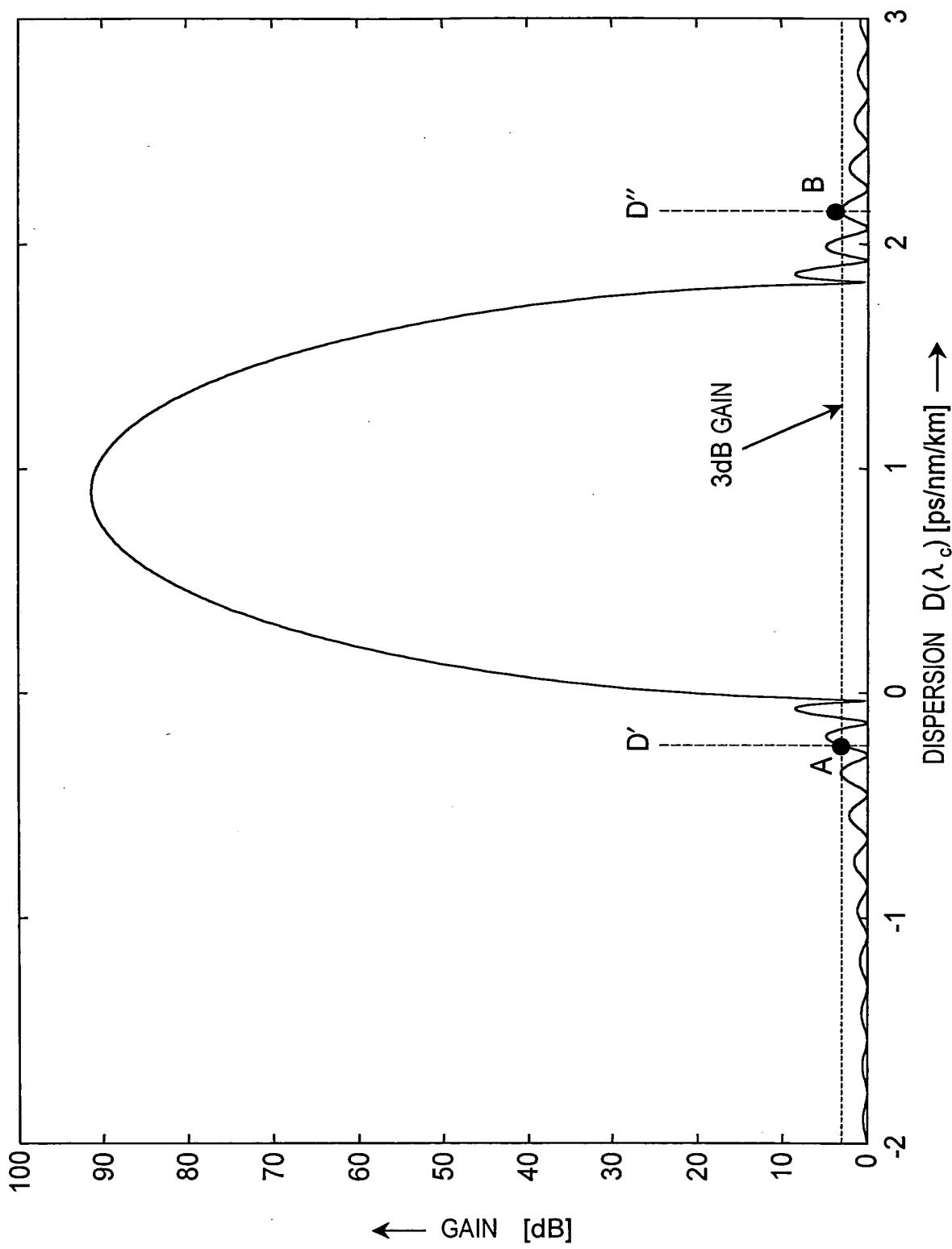
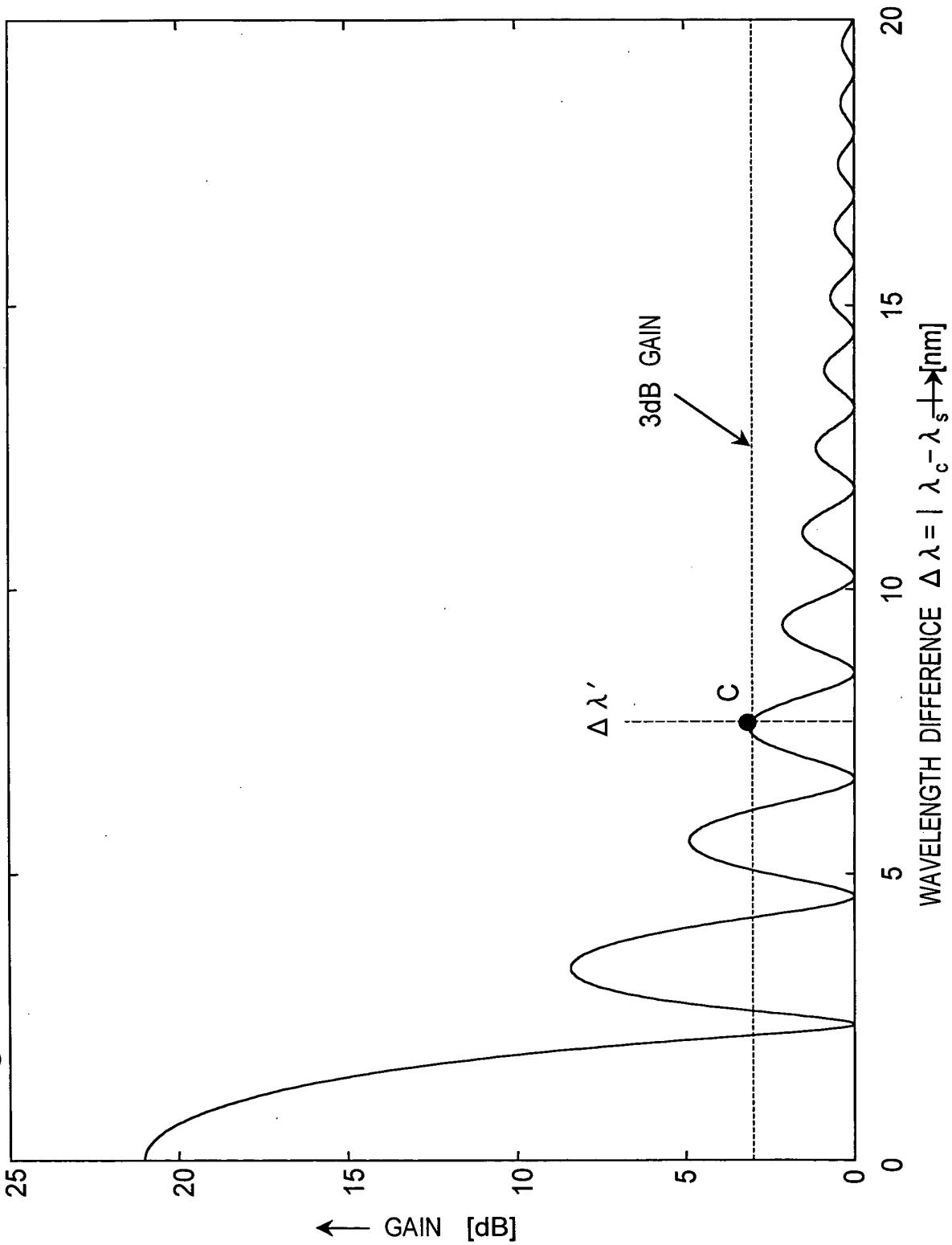
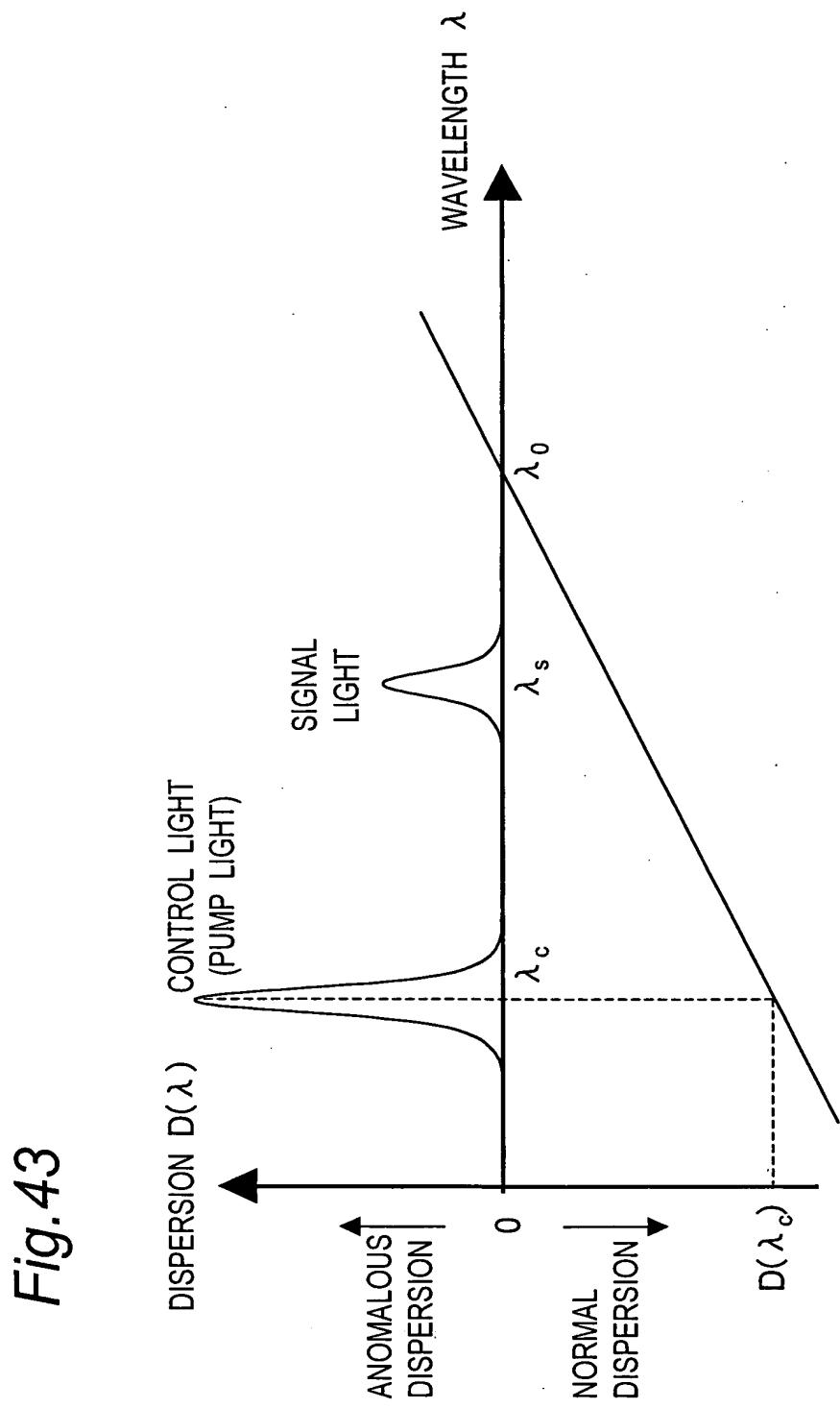


Fig. 42





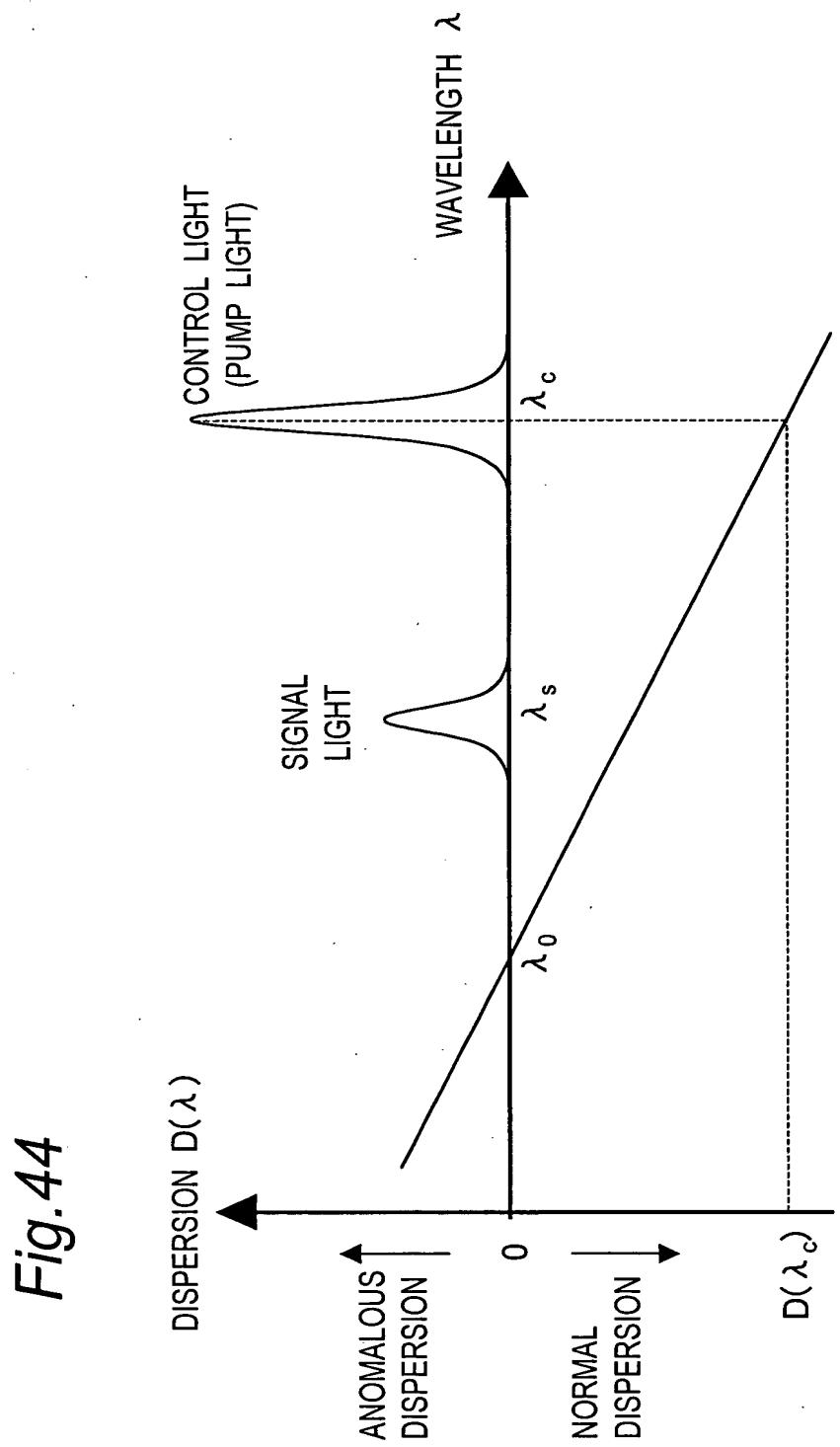
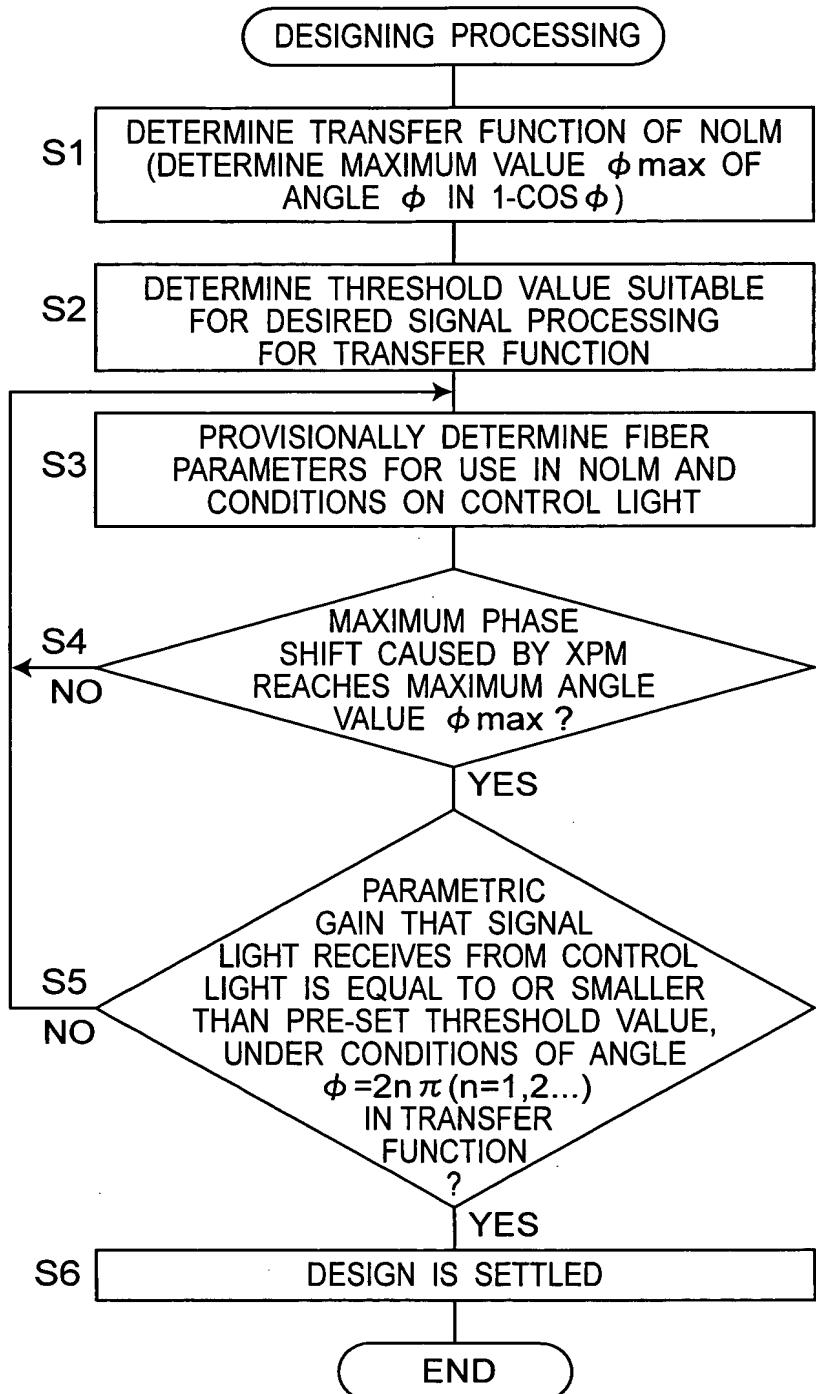


Fig.45



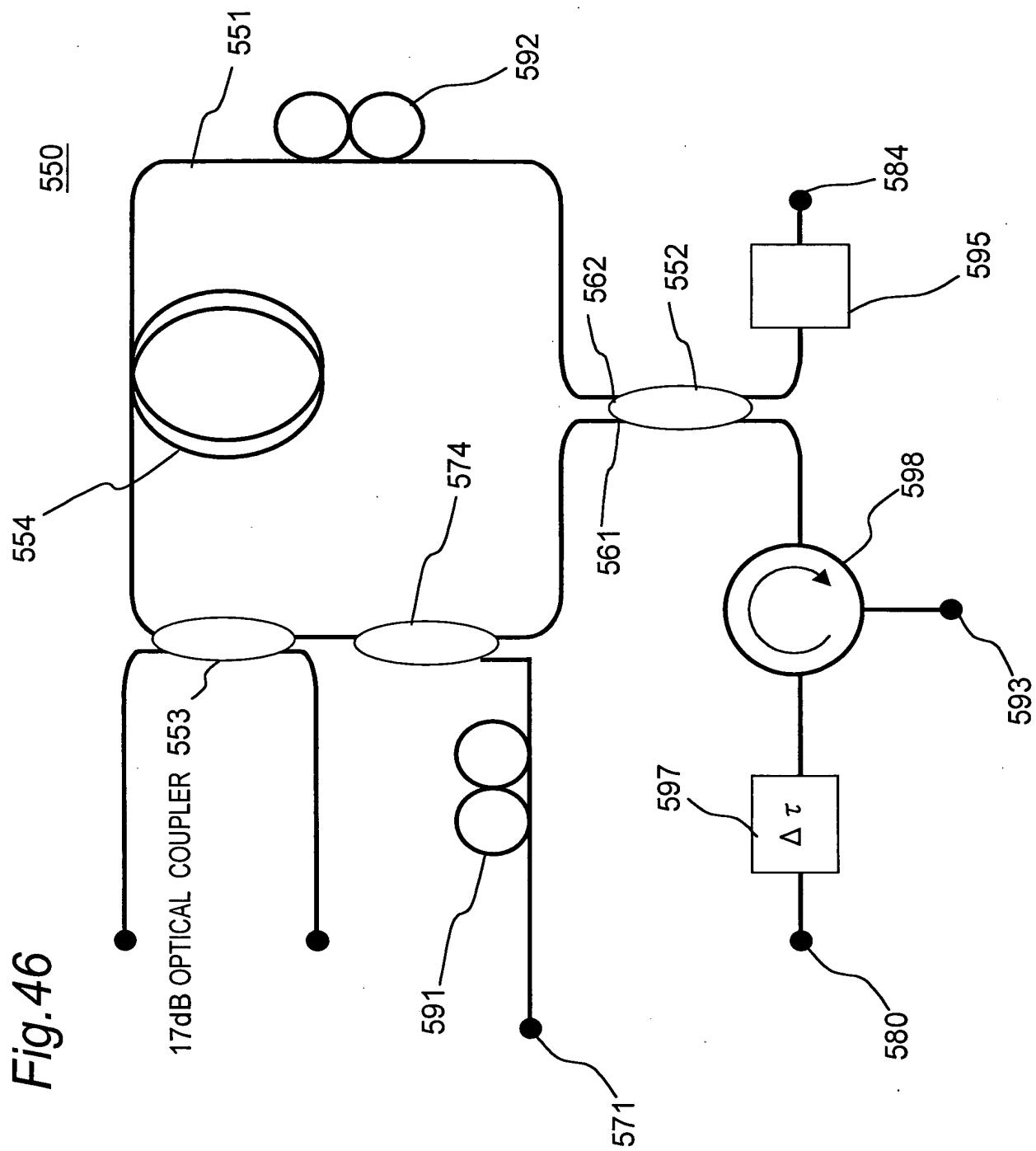


Fig. 47

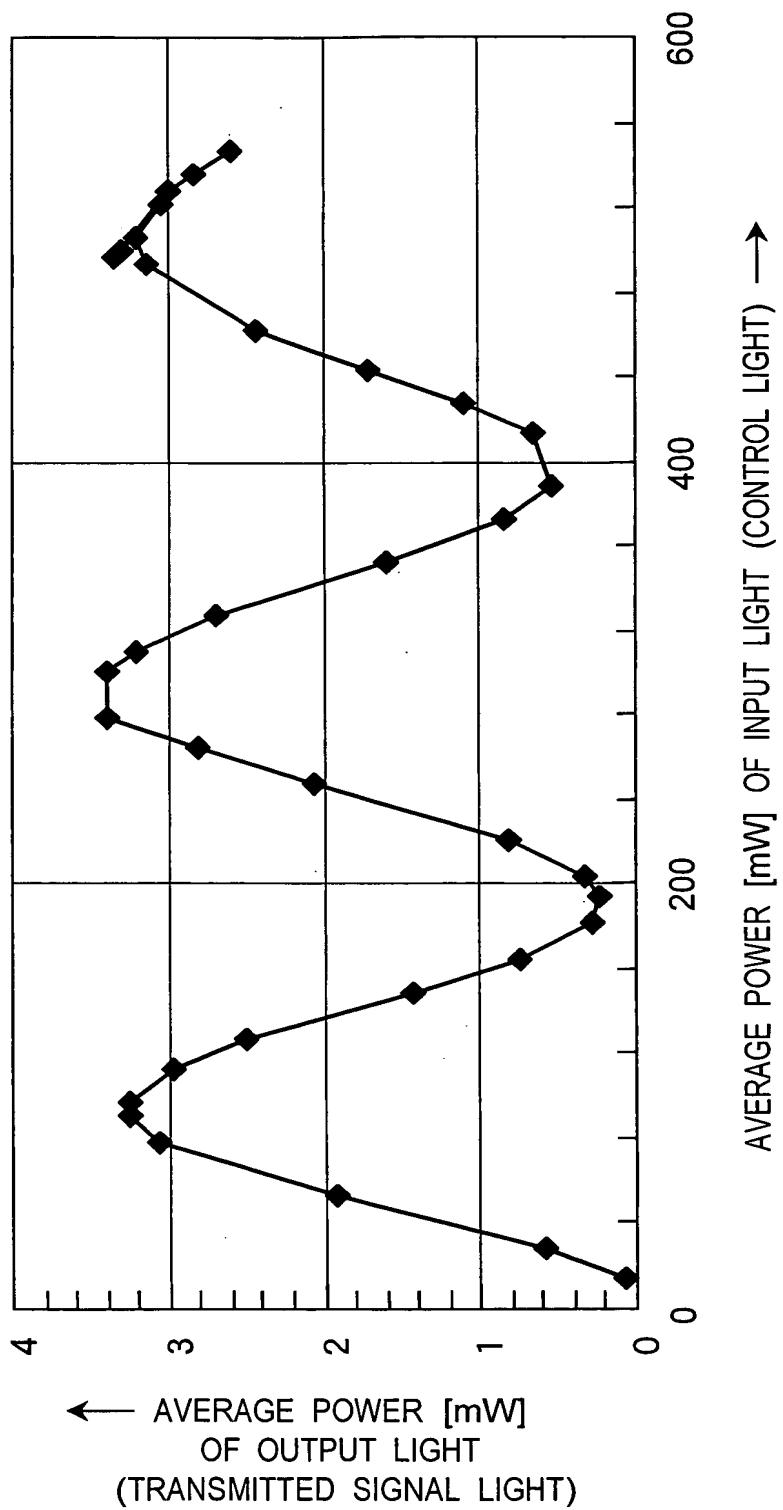


Fig.48

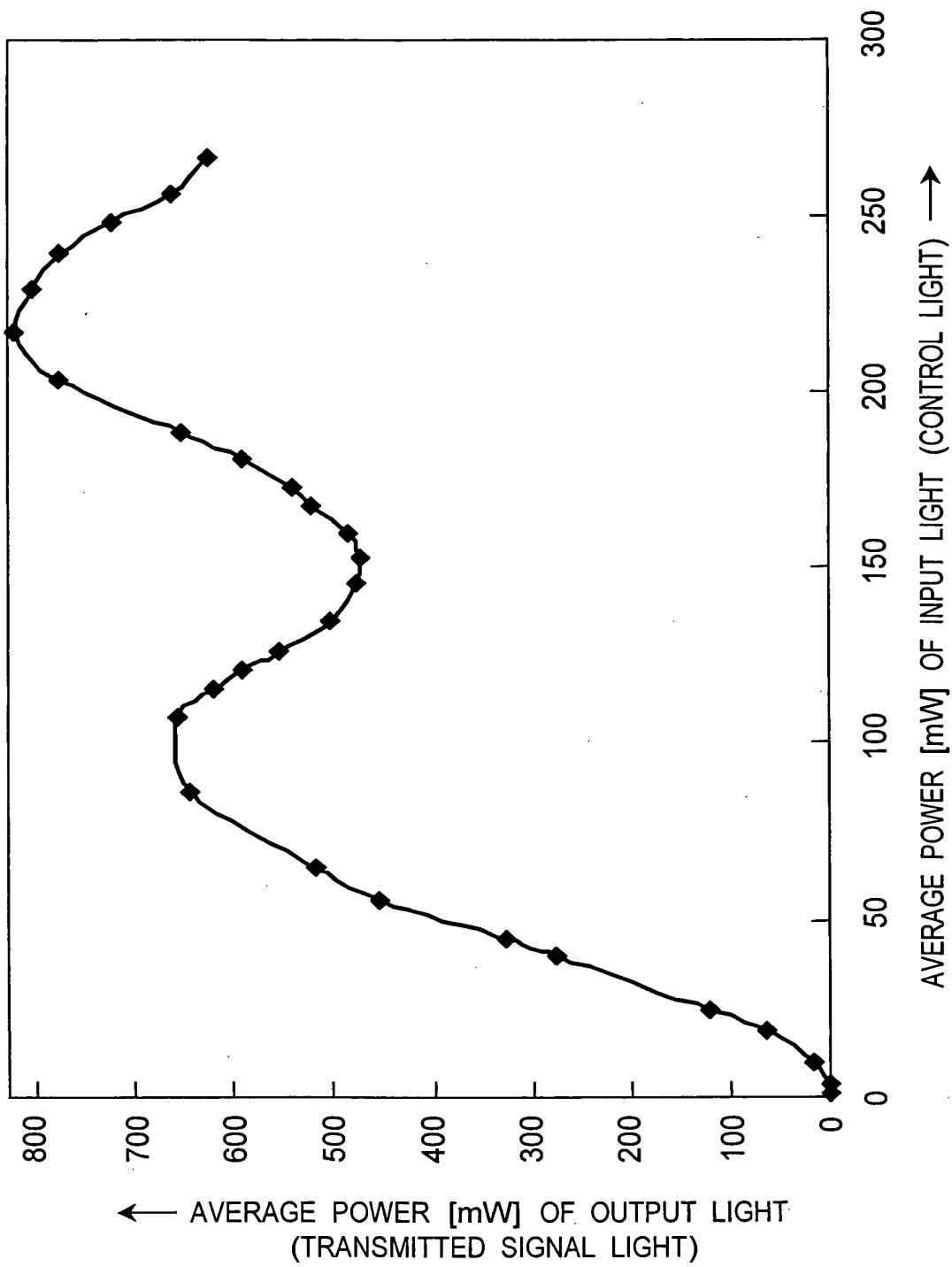


Fig. 49

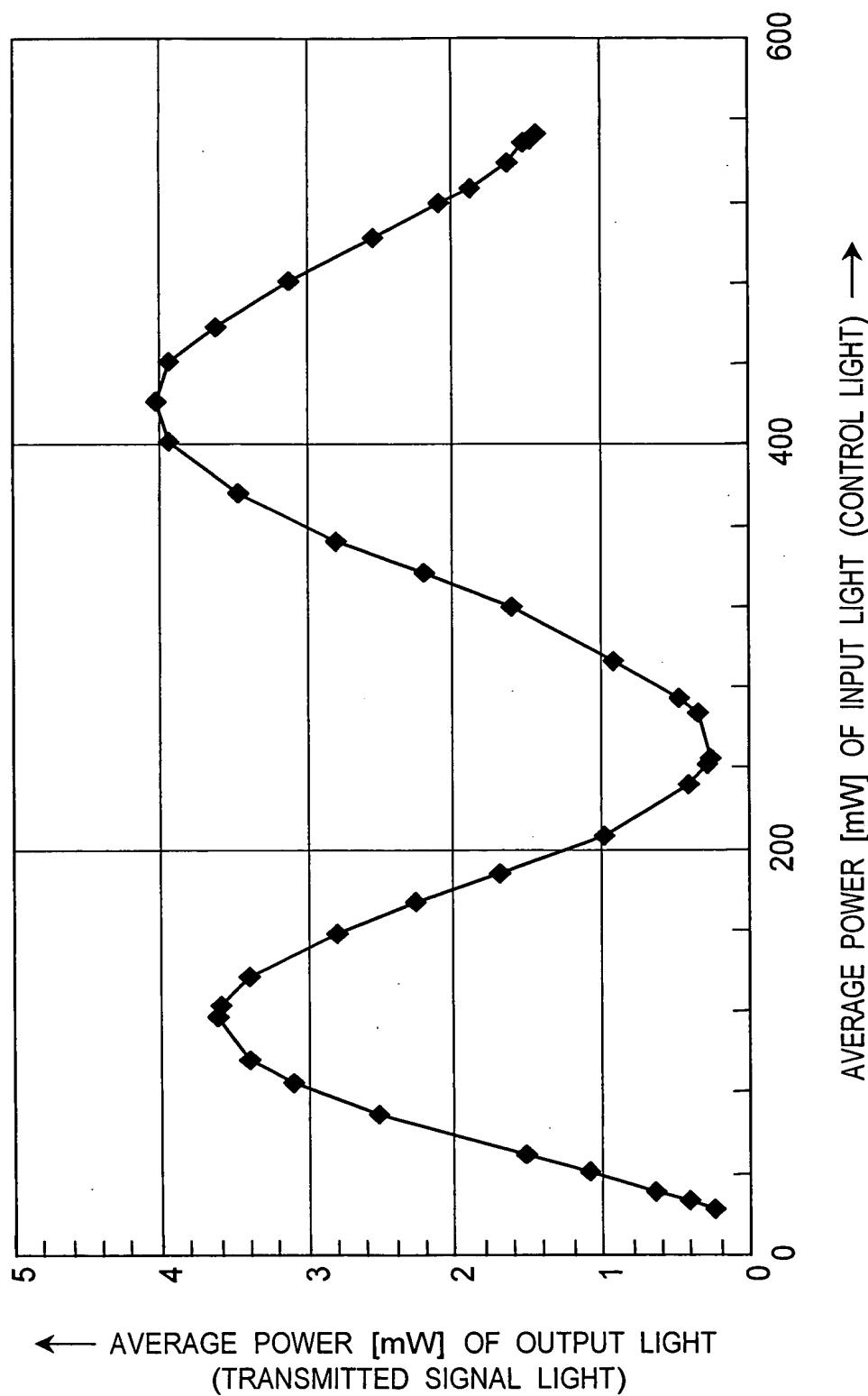
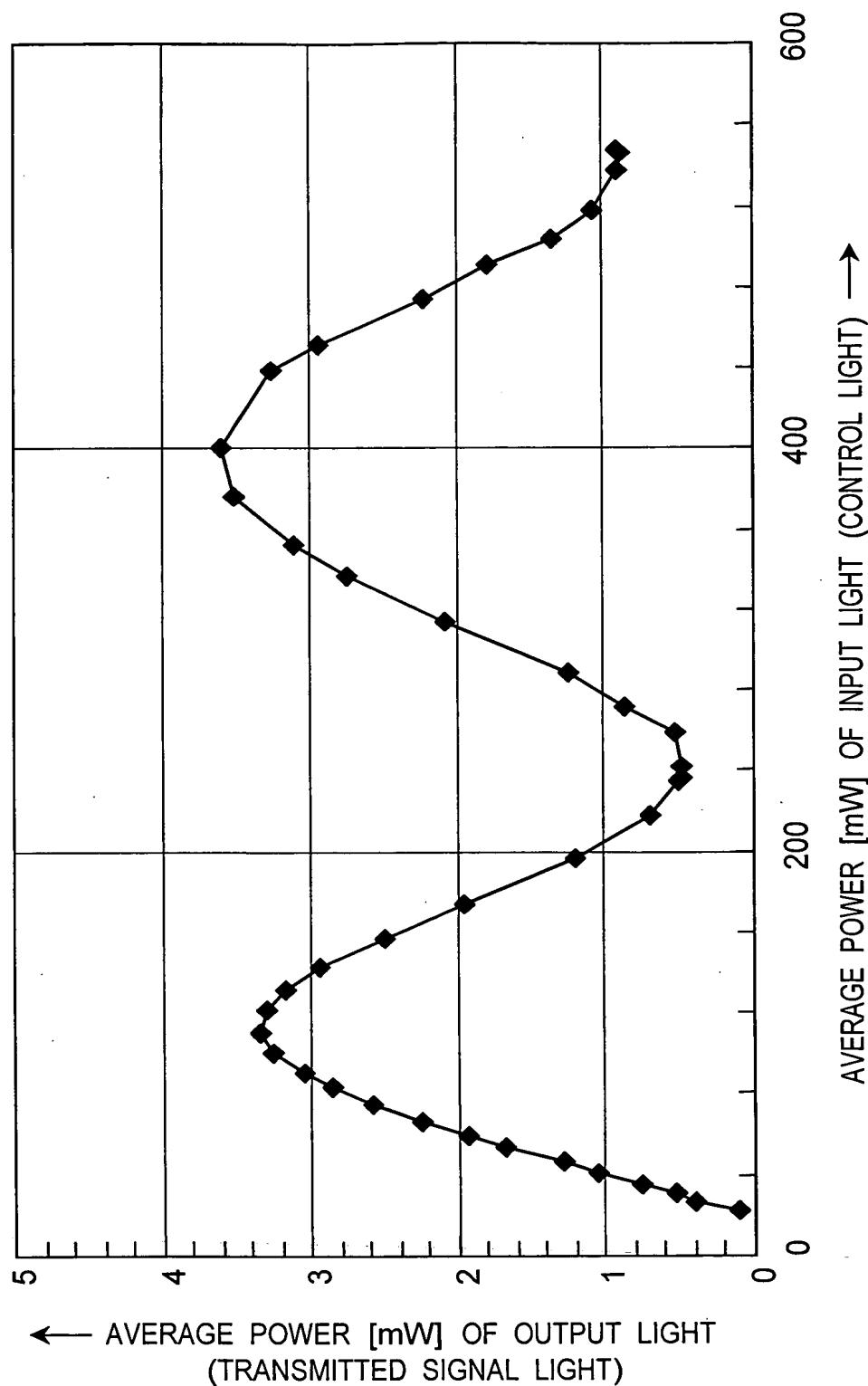


Fig. 50



*Fig. 51*

MULTI-LEVEL OPTICAL SIGNAL DECODER 400

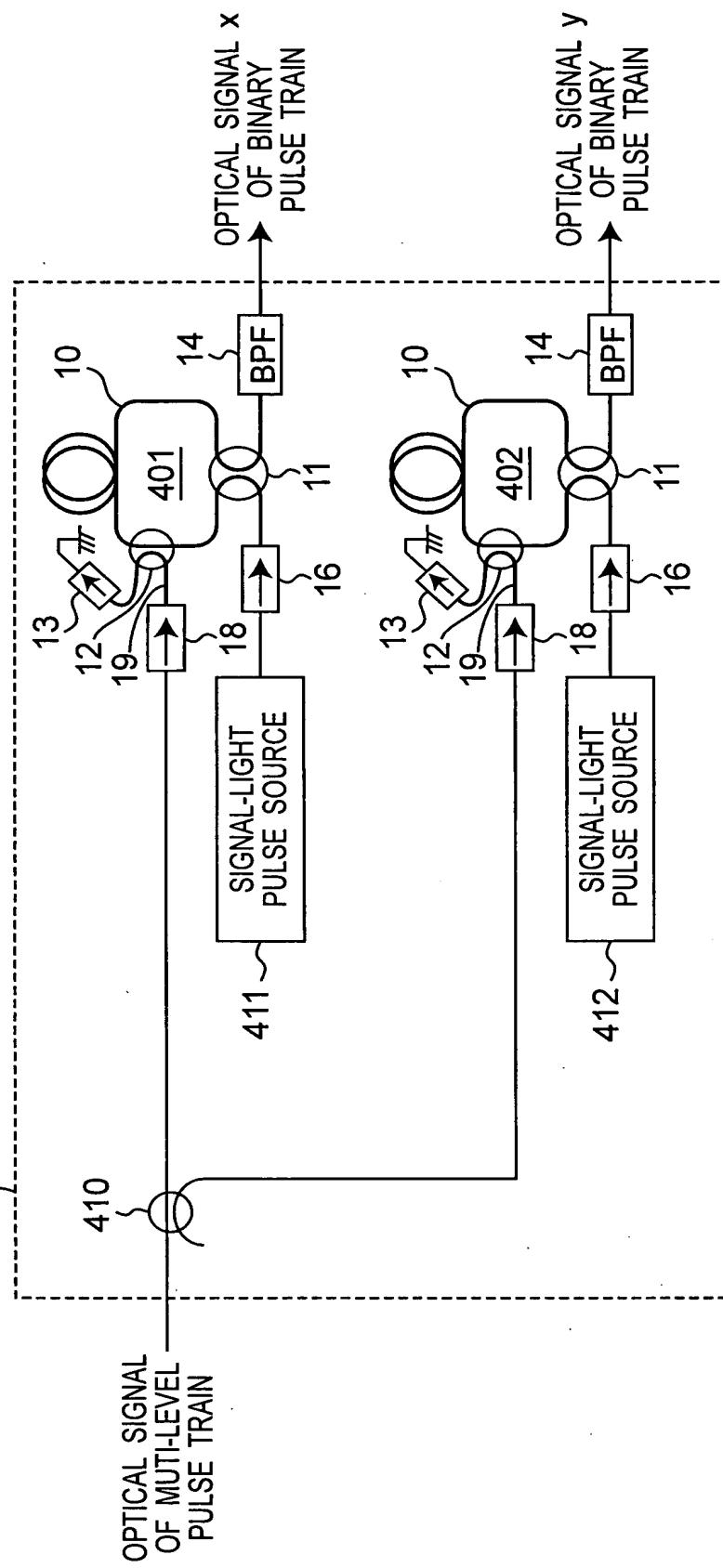


Fig.52

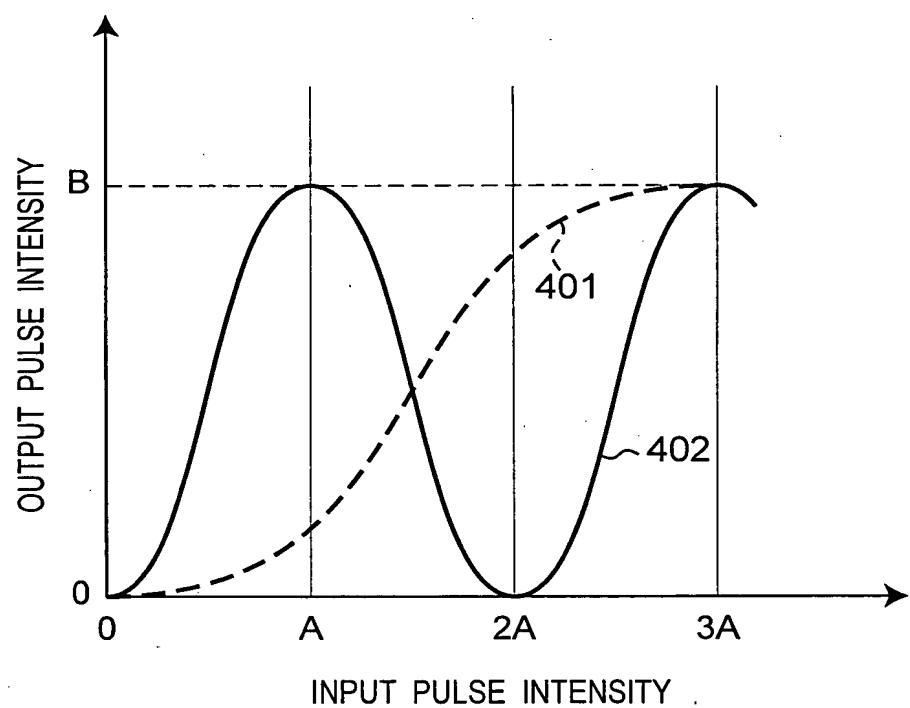
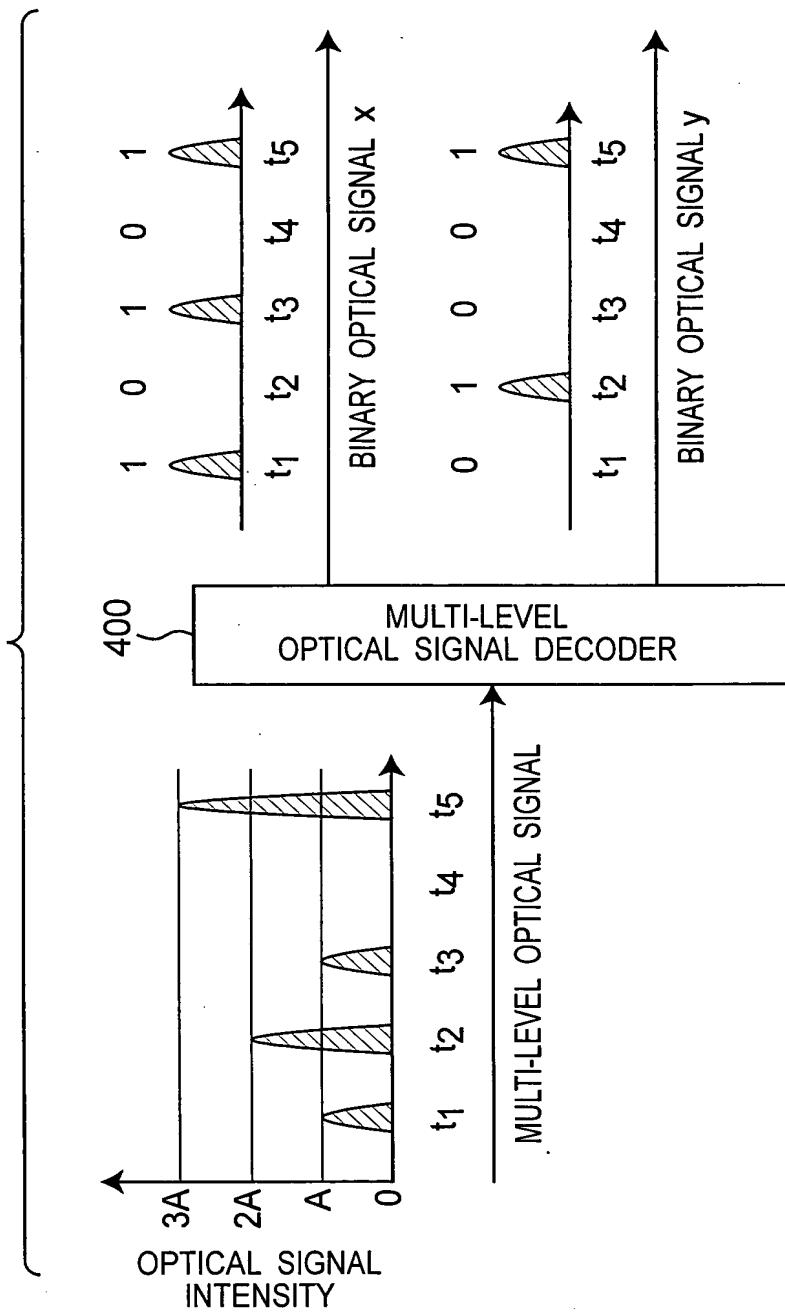


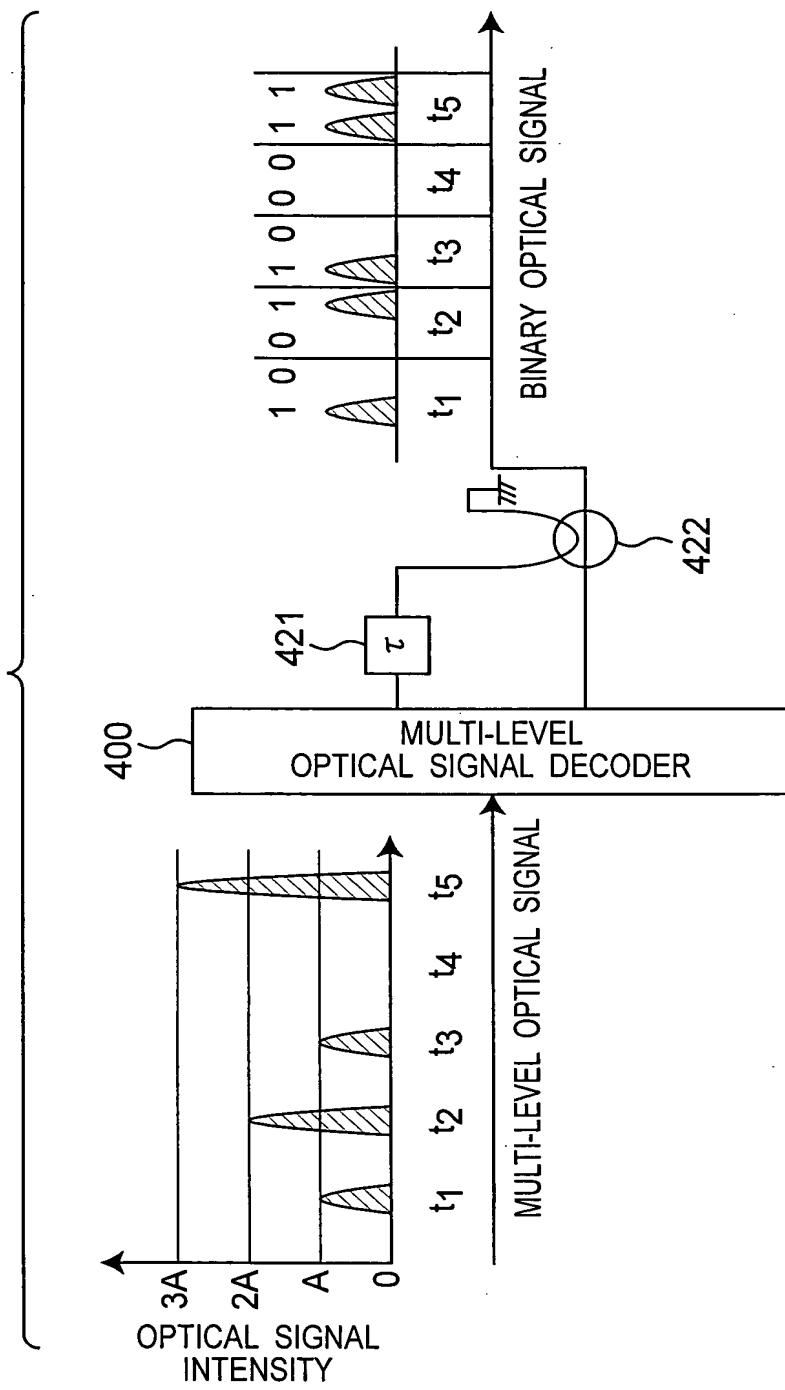
Fig. 53



*Fig.54*

$x$	$y$	OPTICAL SIGNAL INTENSITY
0	0	0
1	0	$A$
0	1	$2A$
1	1	$3A$

Fig. 55



*Fig.56*

CODE	OPTICAL SIGNAL INTENSITY
00	0
10	A
01	2A
11	3A

Fig.57

## OPTICAL LOGIC OPERATION CIRCUIT 600

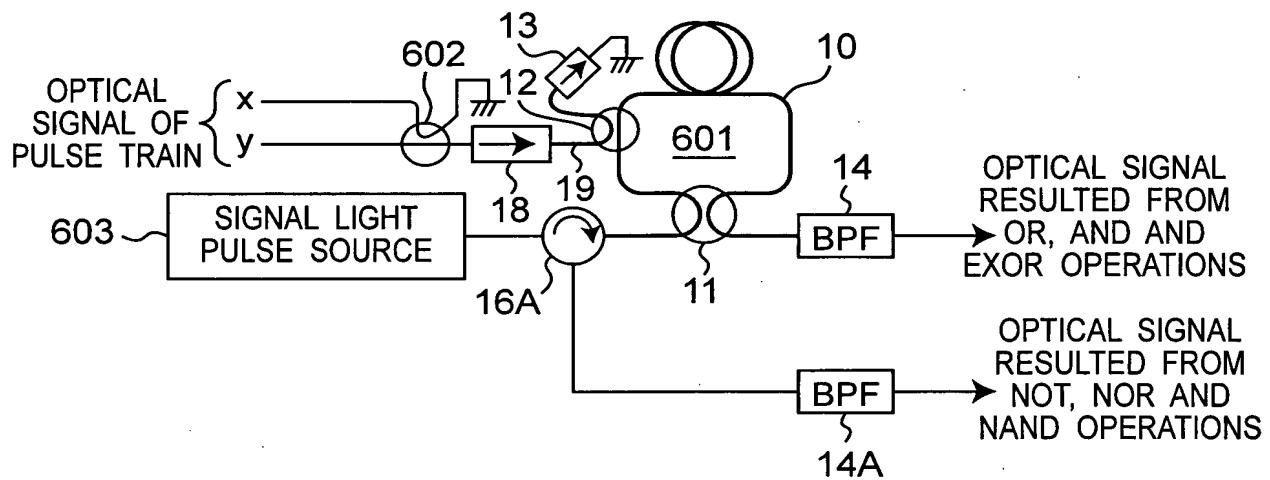


Fig.58

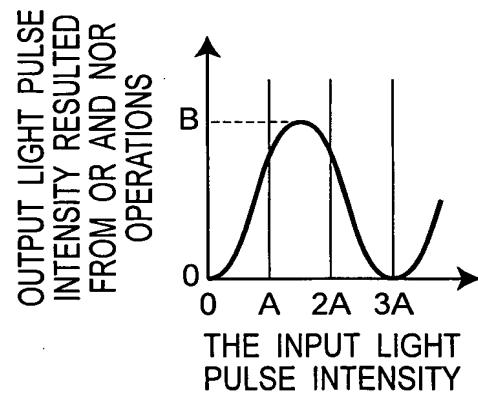


Fig.59

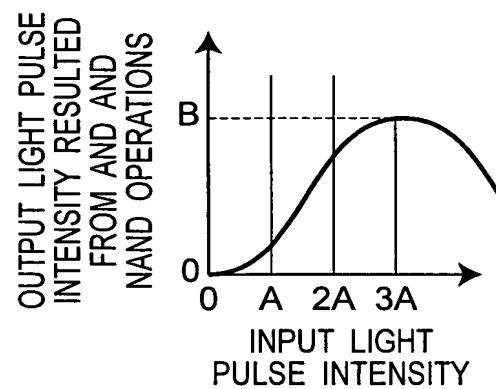


Fig.60

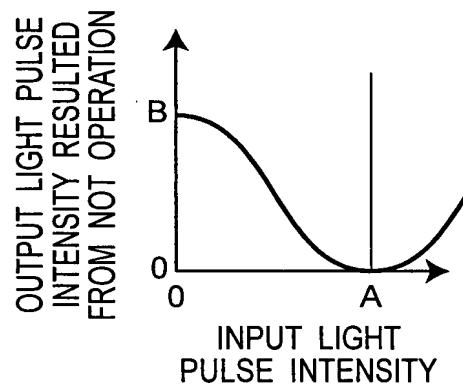


Fig.61

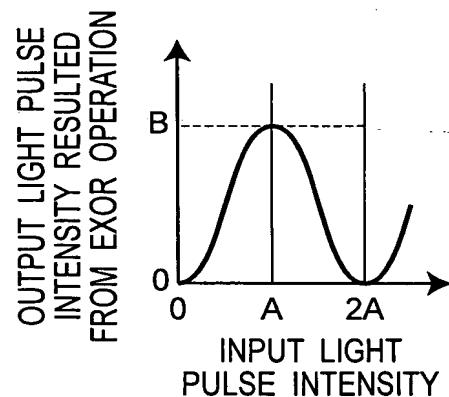


Fig. 62

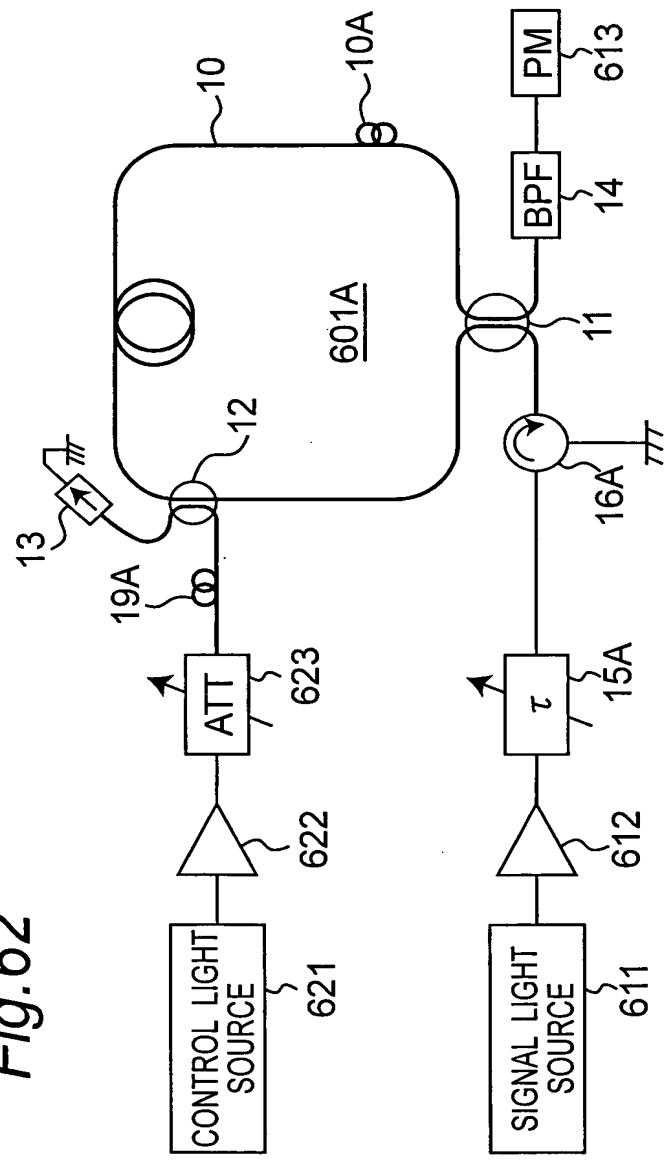
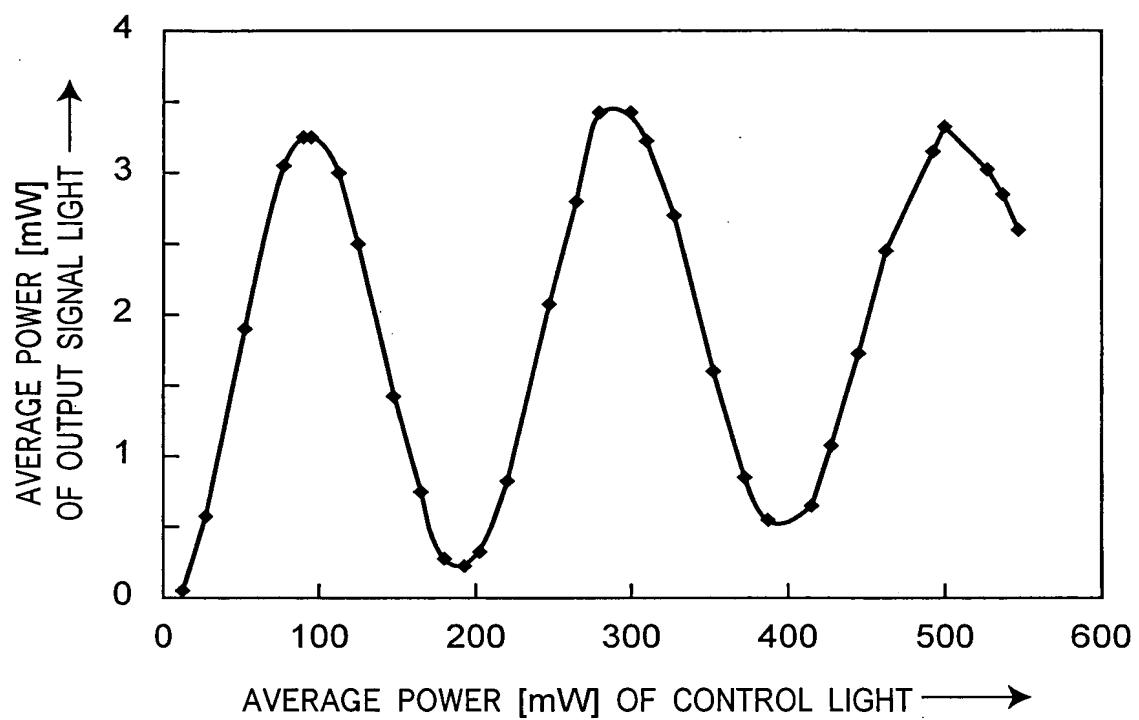


Fig. 63



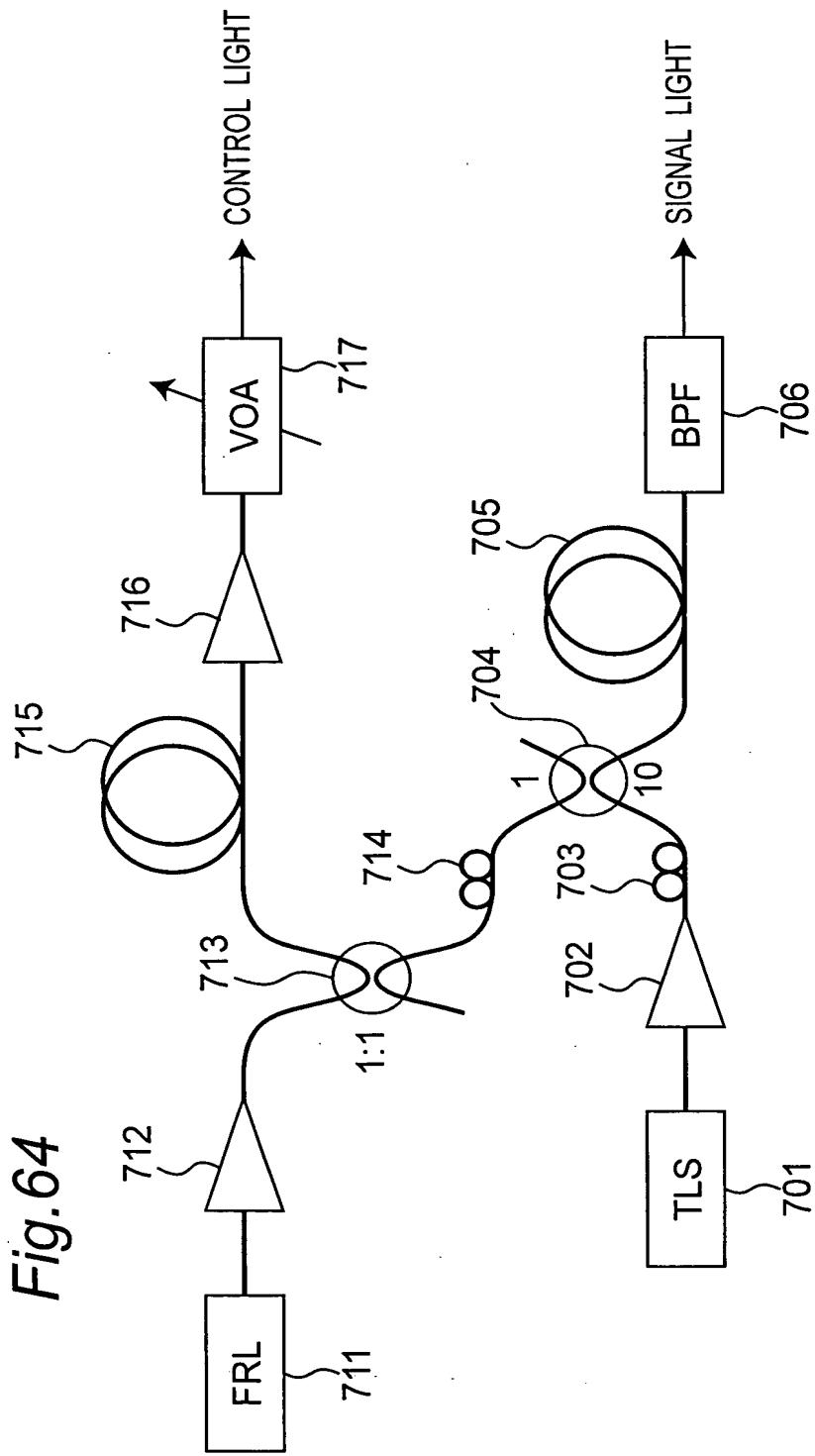


Fig. 65

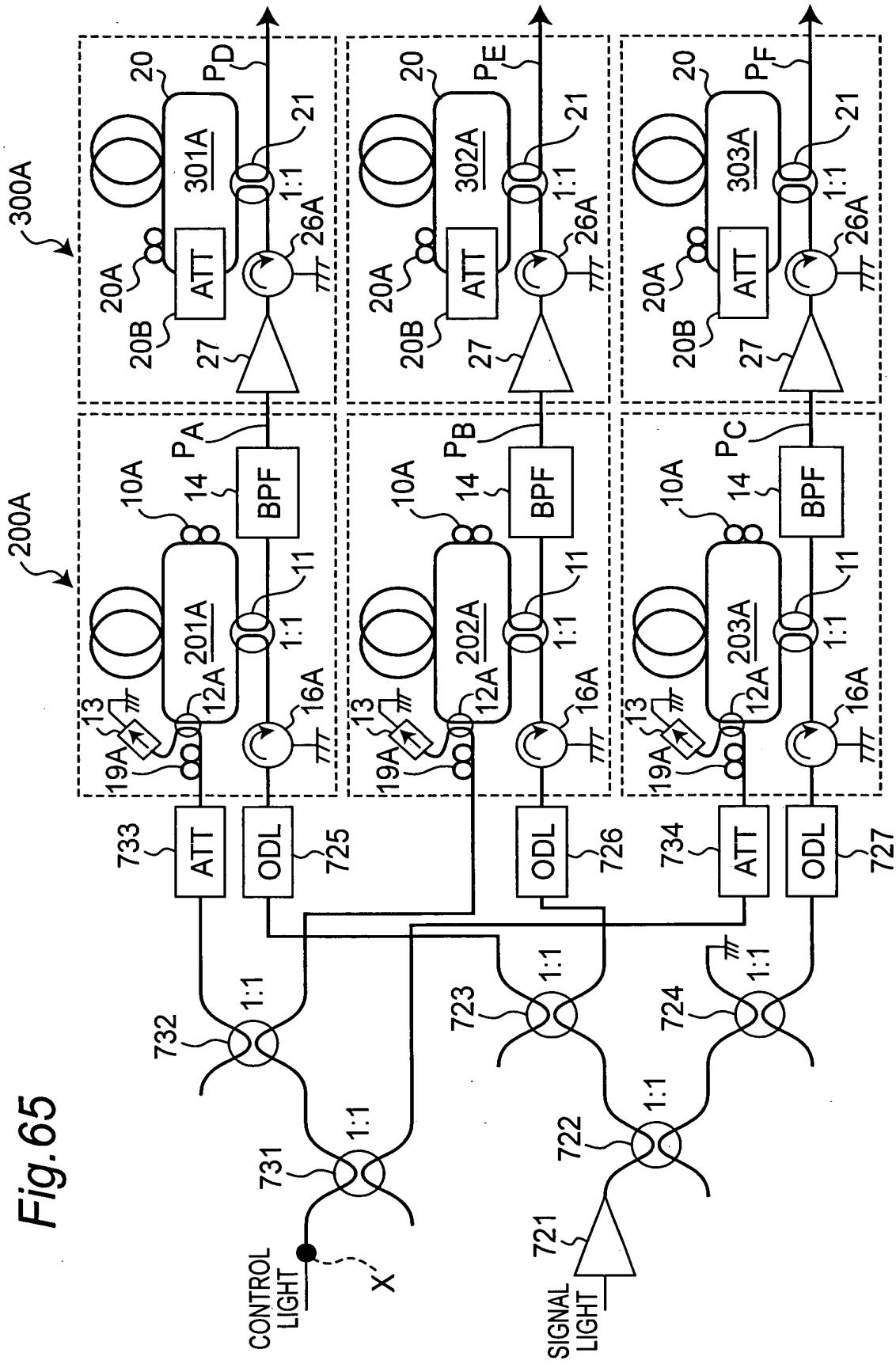


Fig. 66

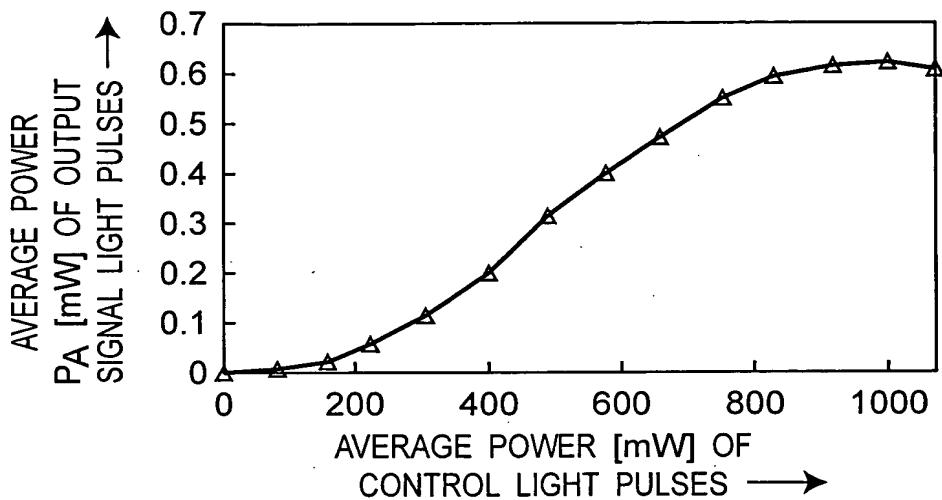


Fig. 67

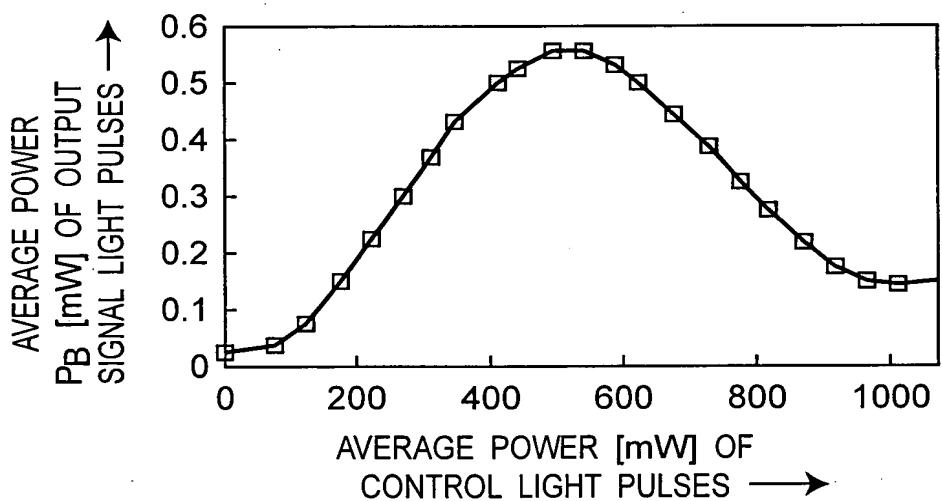


Fig. 68

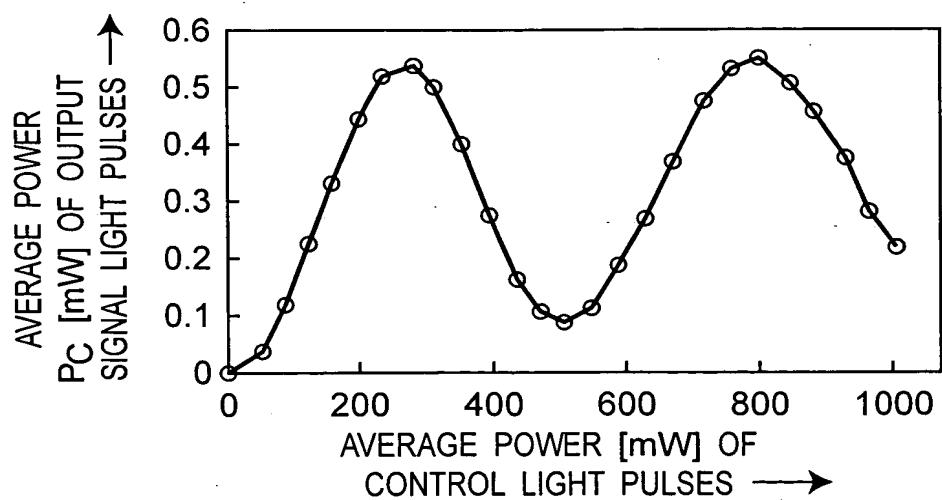


Fig. 69

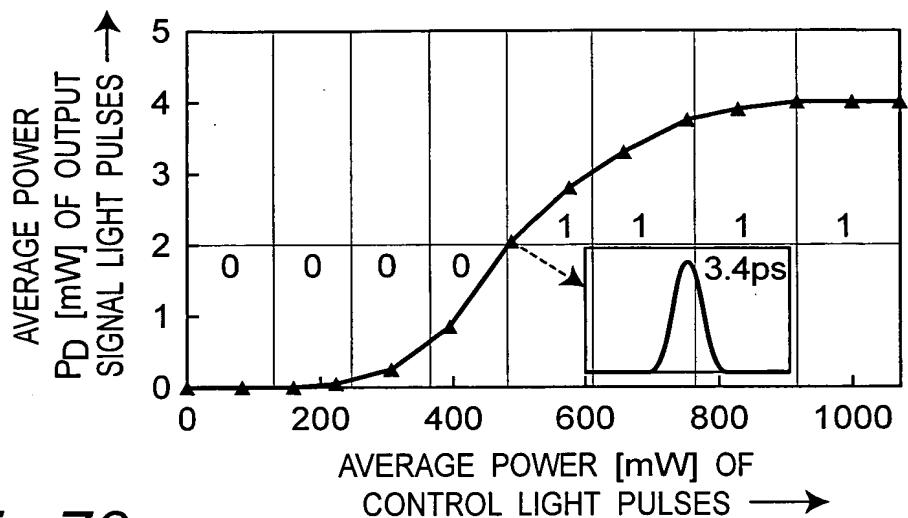


Fig. 70

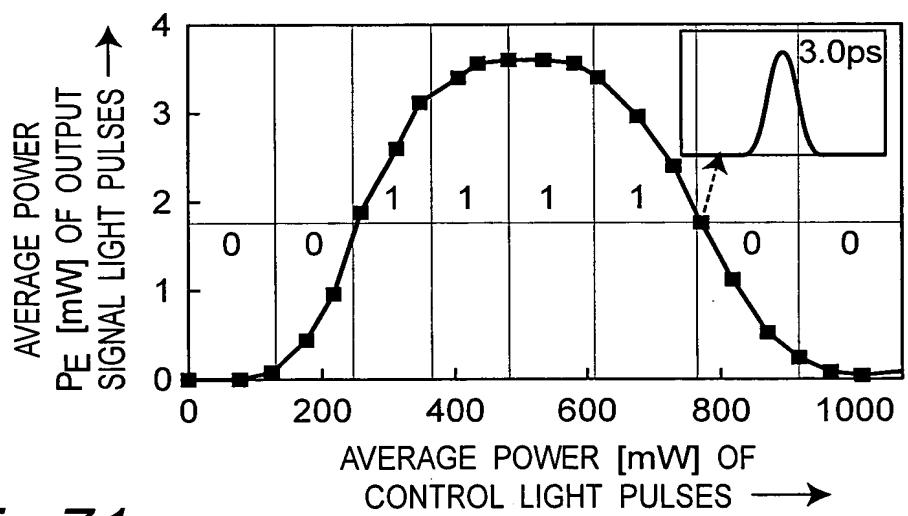
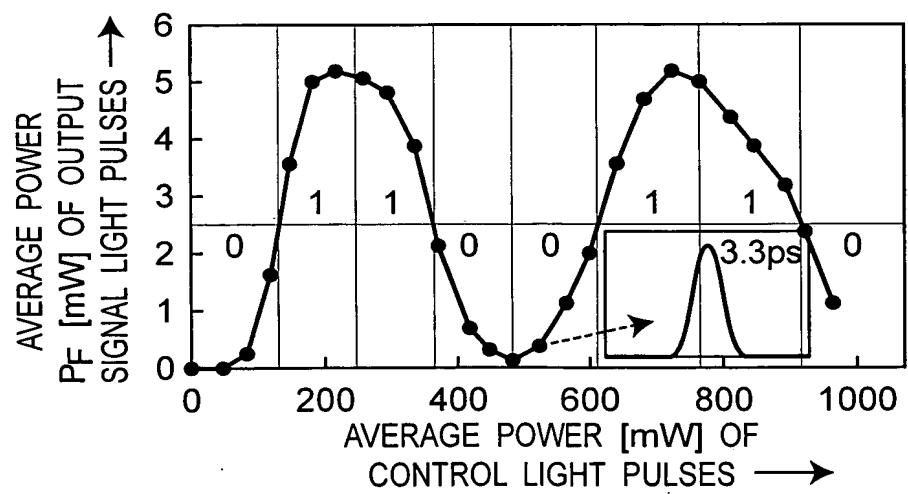


Fig. 71



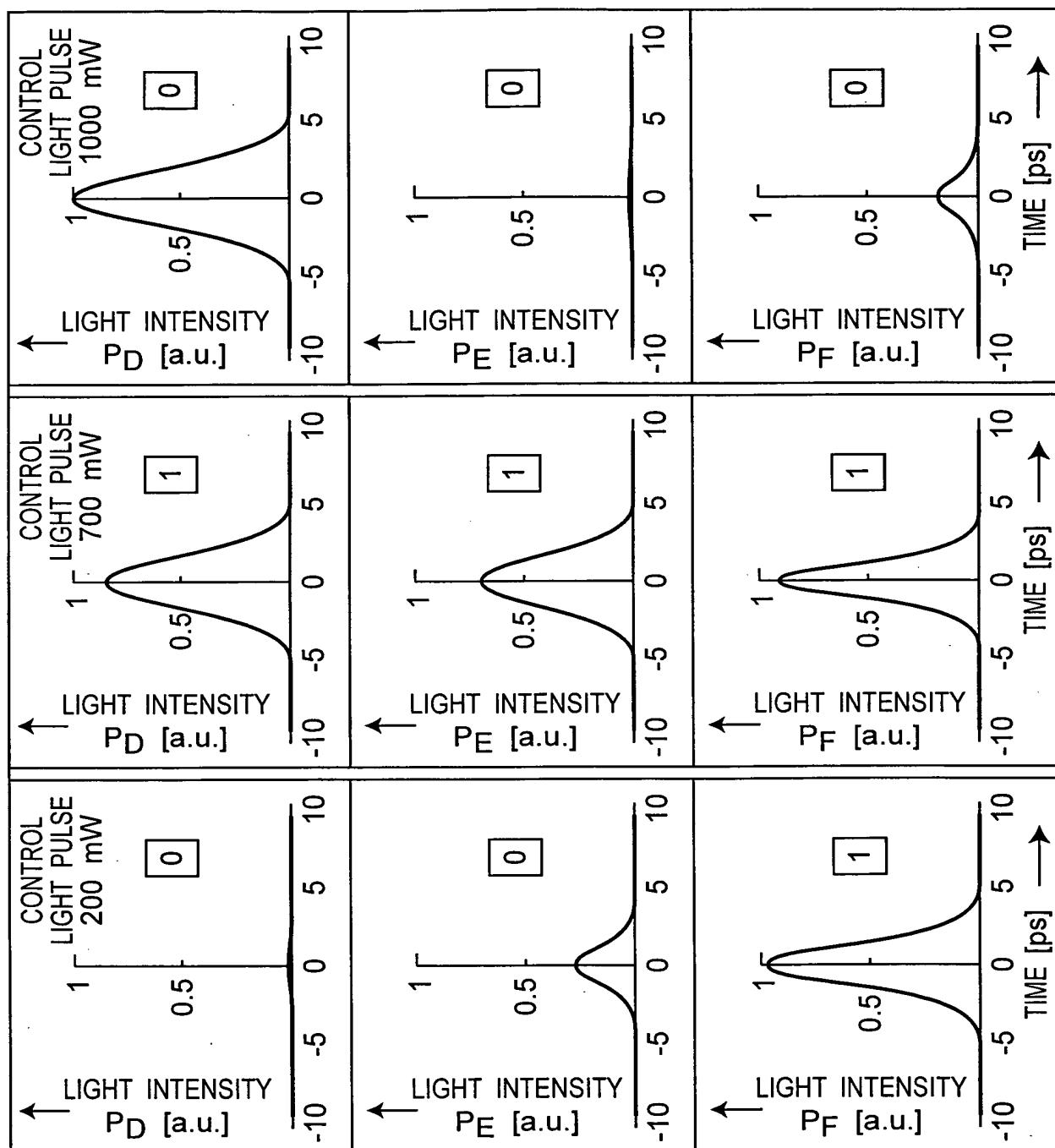


Fig. 7.2